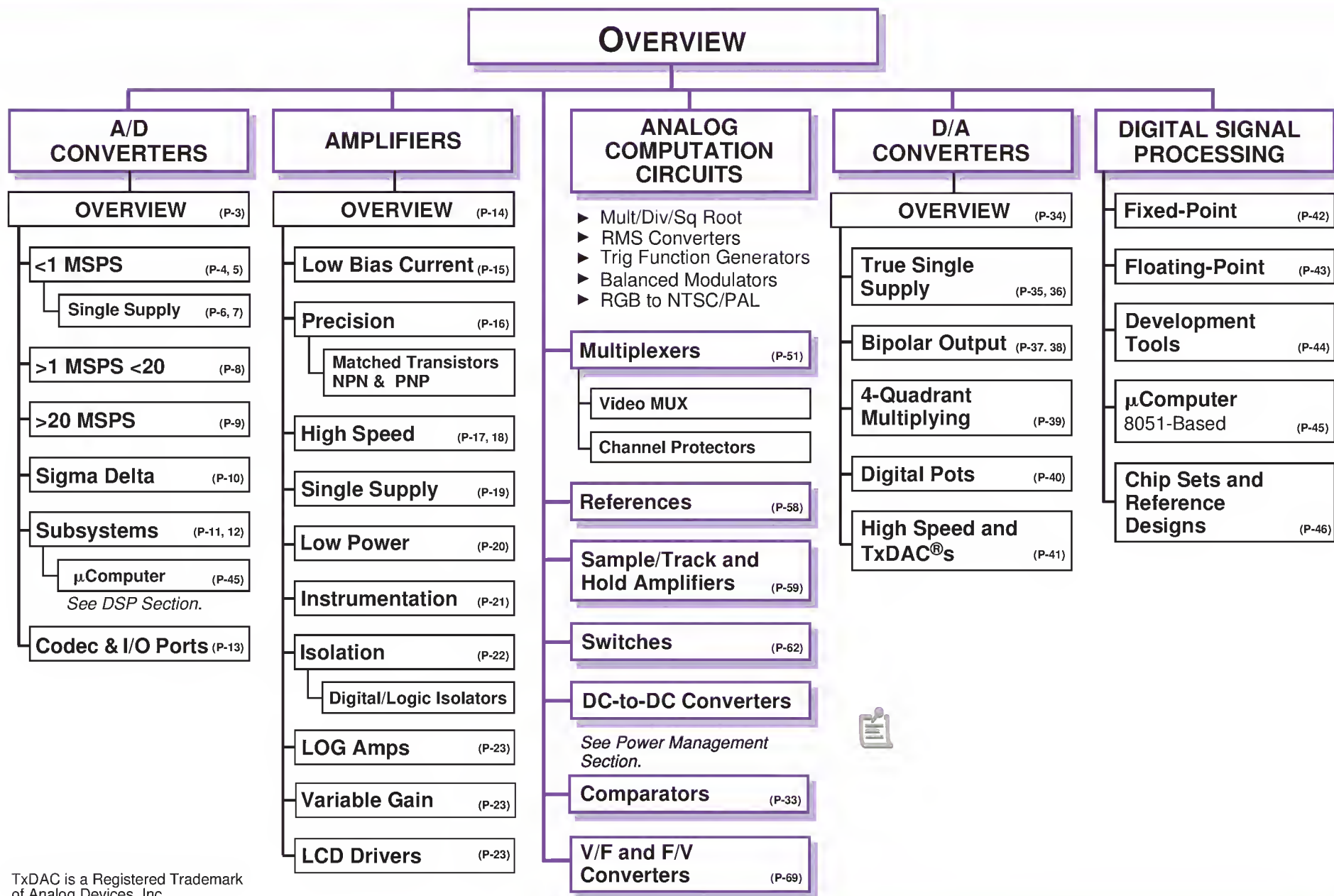
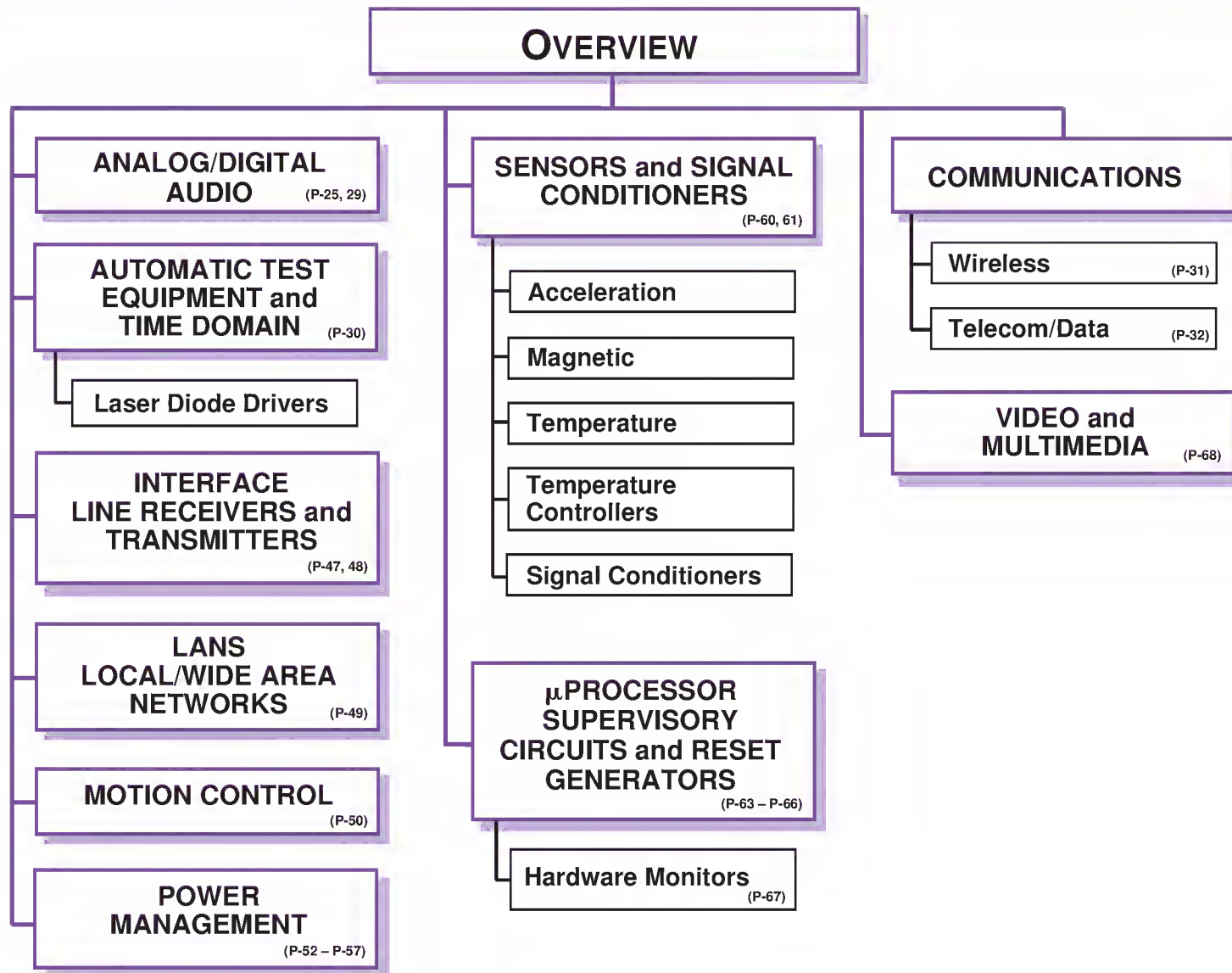


GENERAL PURPOSE COMPONENTS

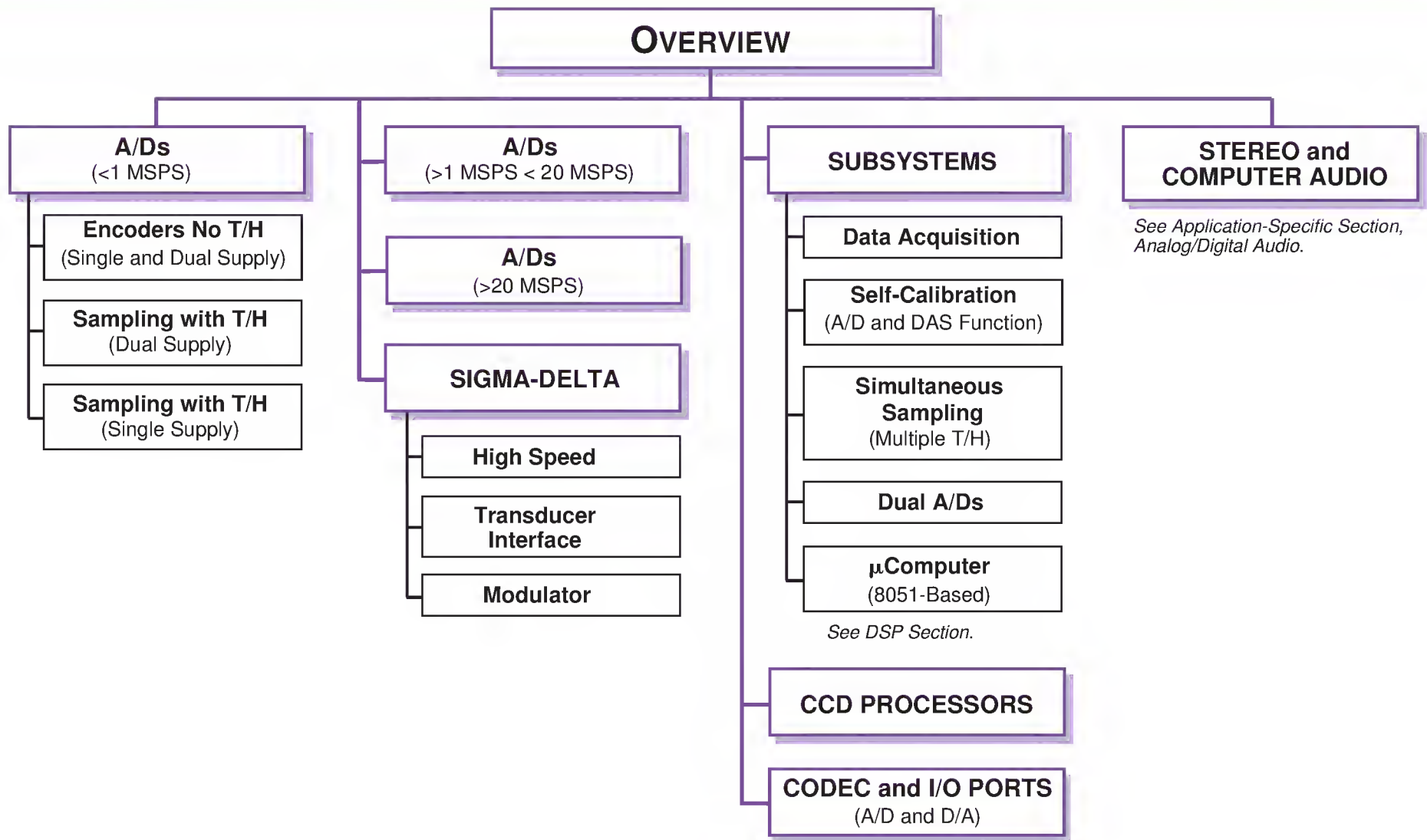


TxDAC is a Registered Trademark of Analog Devices, Inc.

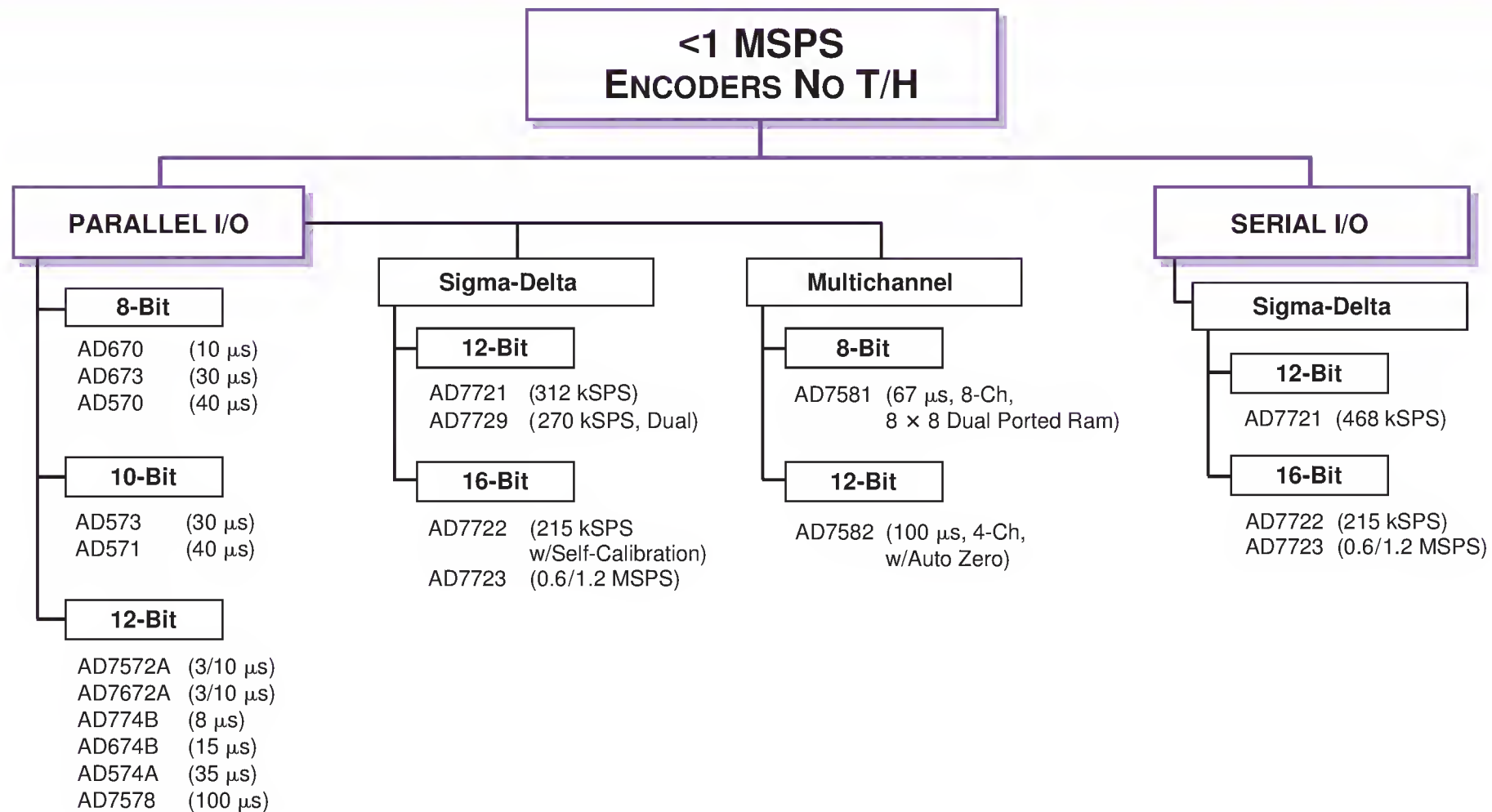
APPLICATION SPECIFIC CIRCUITS



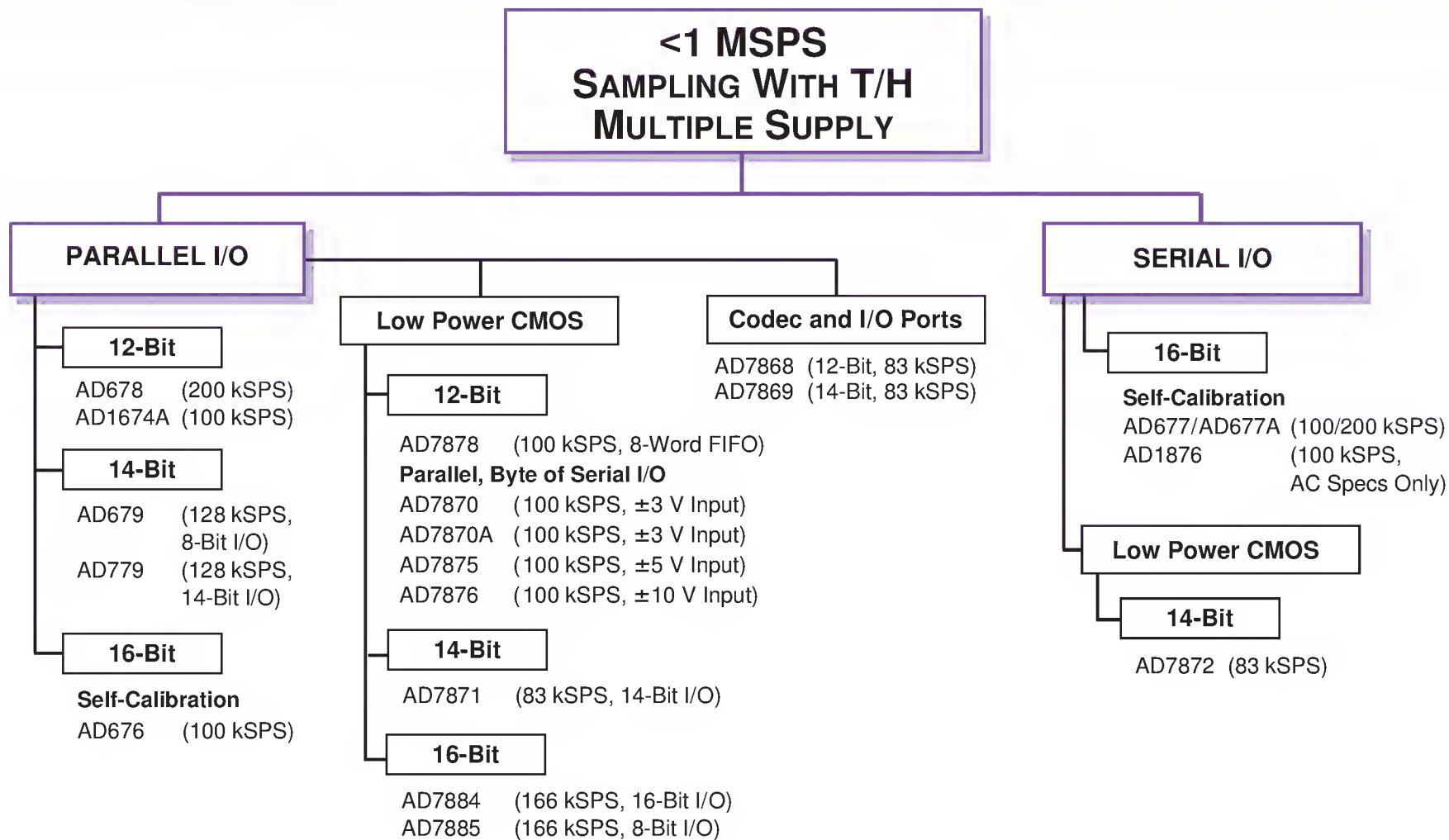
A/D CONVERTERS



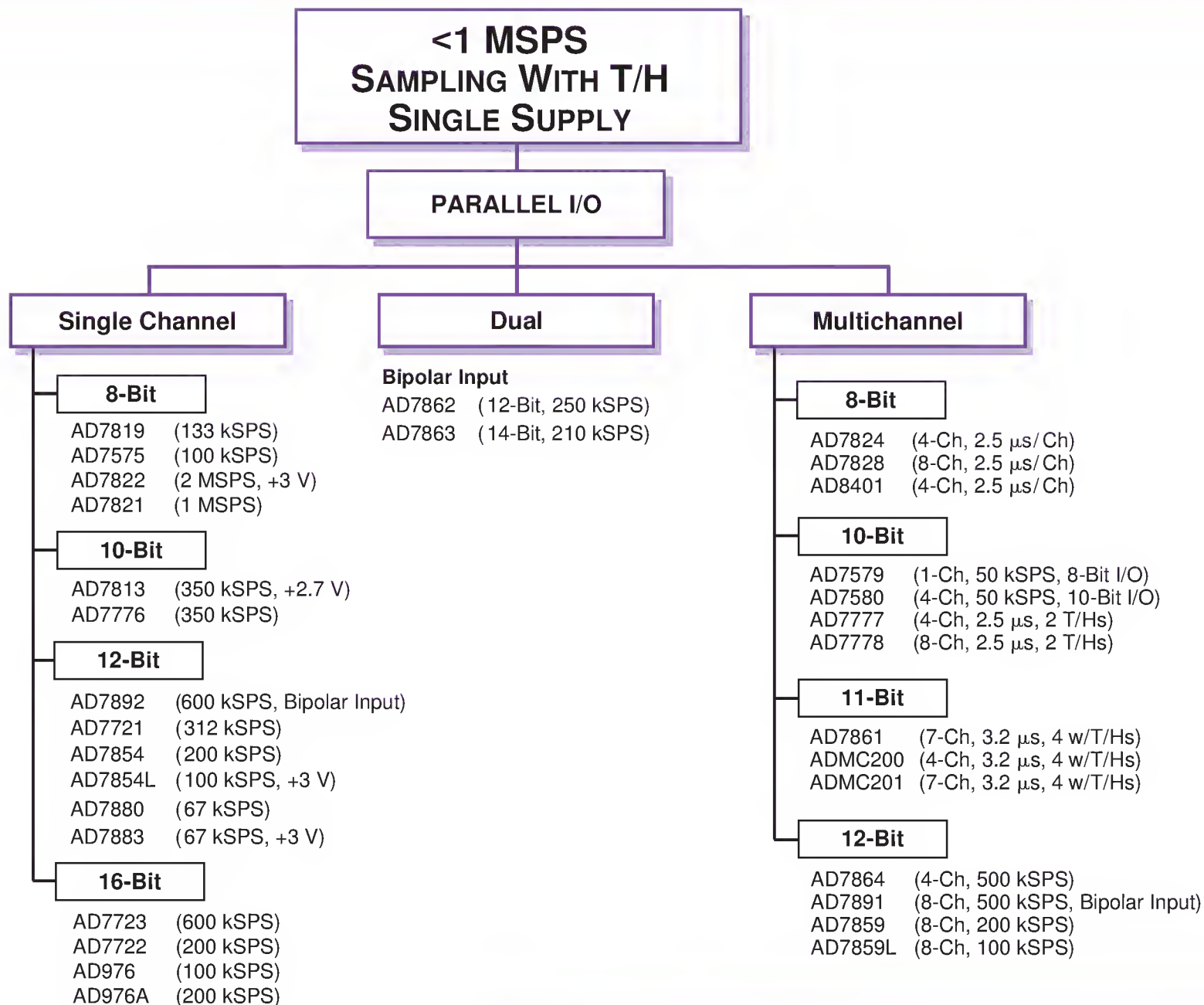
A/D CONVERTERS



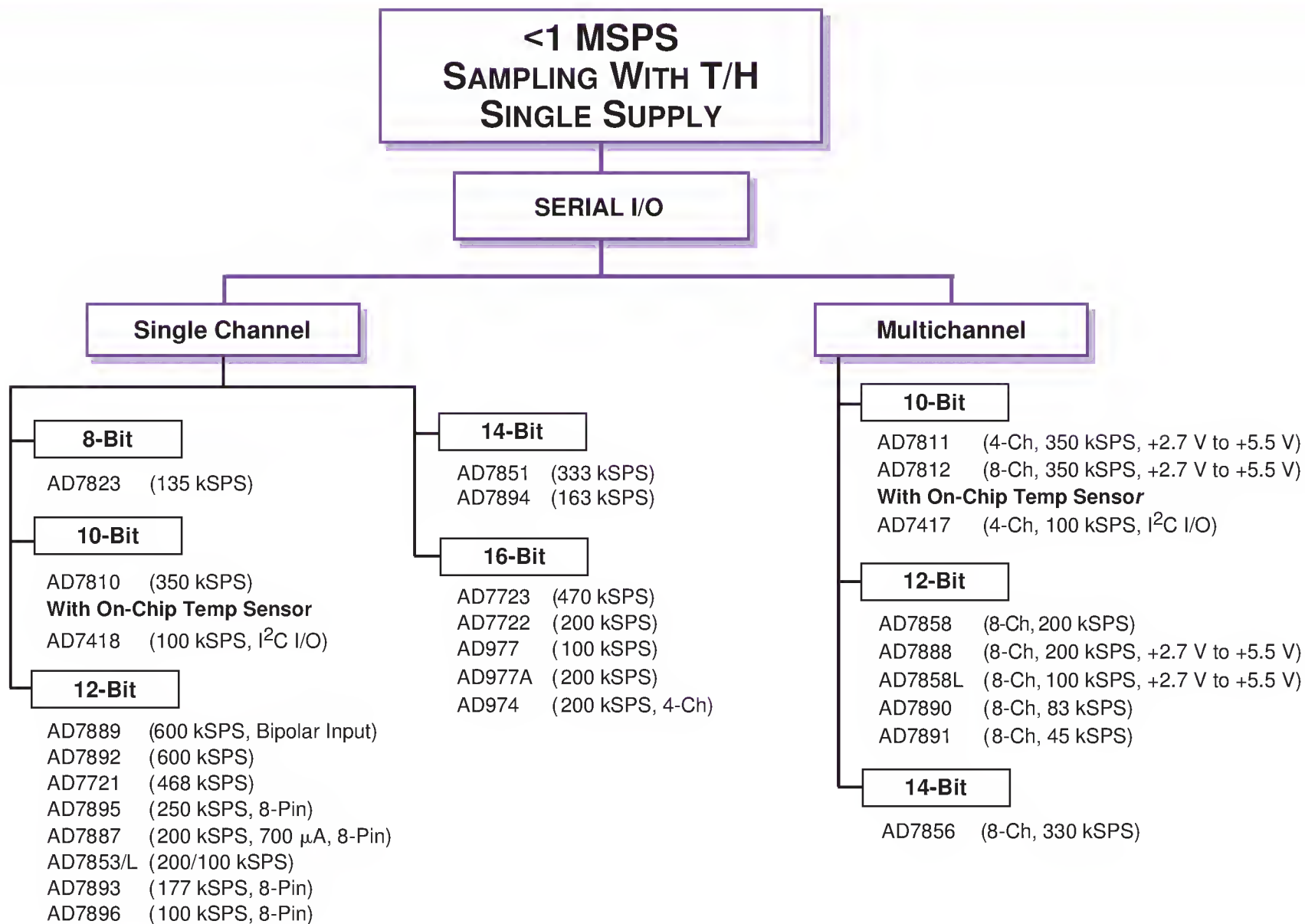
A/D CONVERTERS



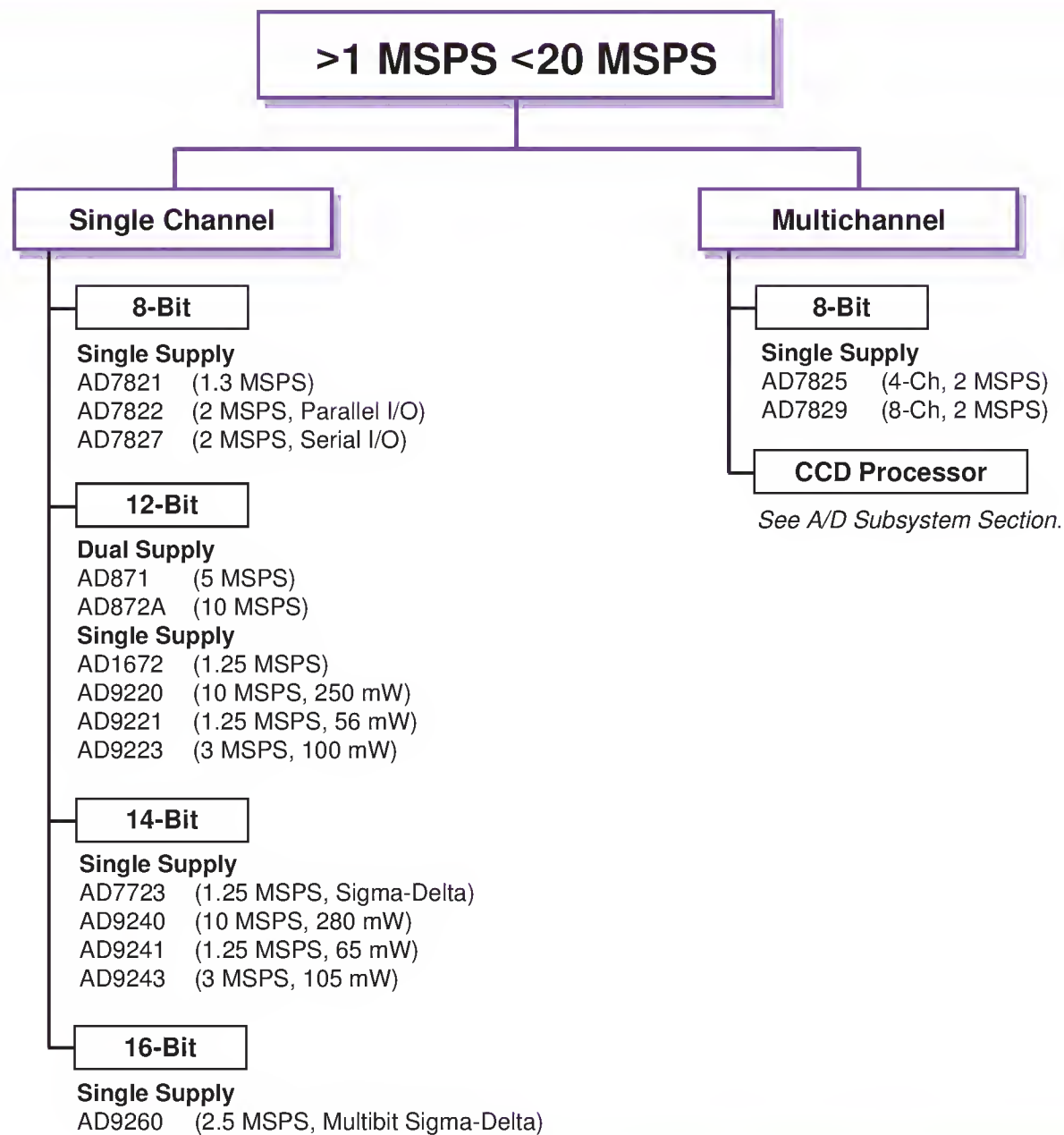
A/D CONVERTERS



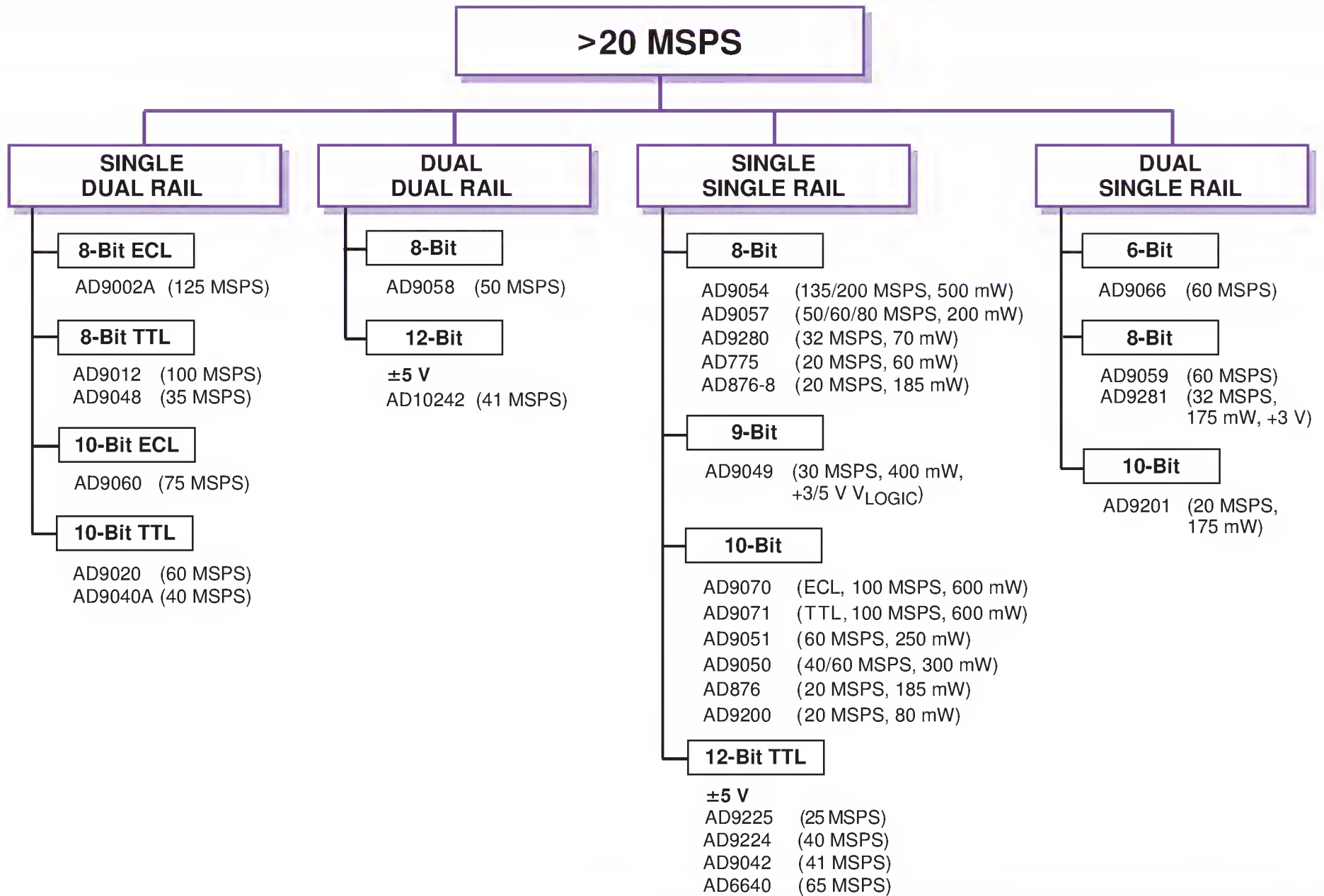
A/D CONVERTERS



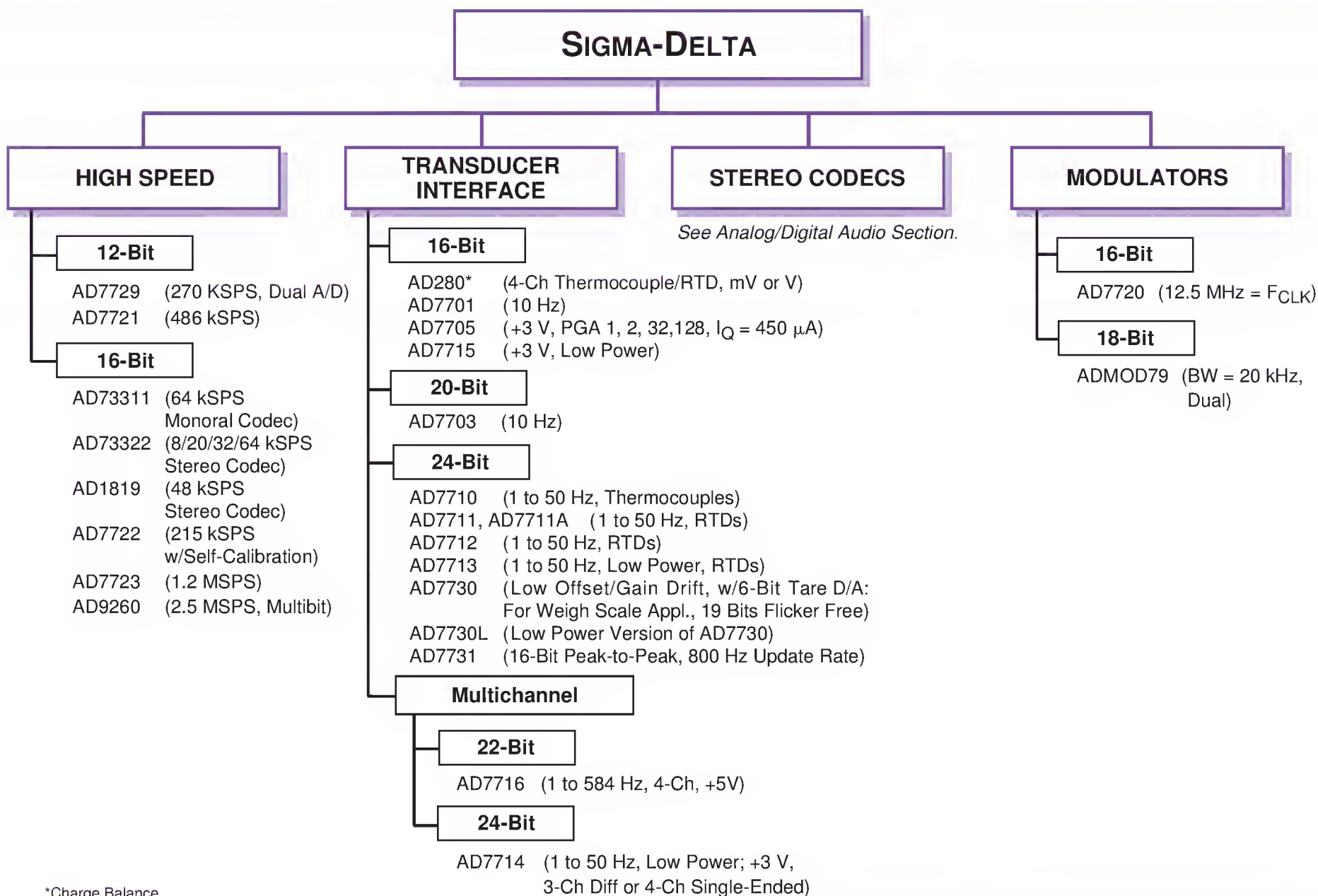
A/D CONVERTERS



A/D CONVERTERS

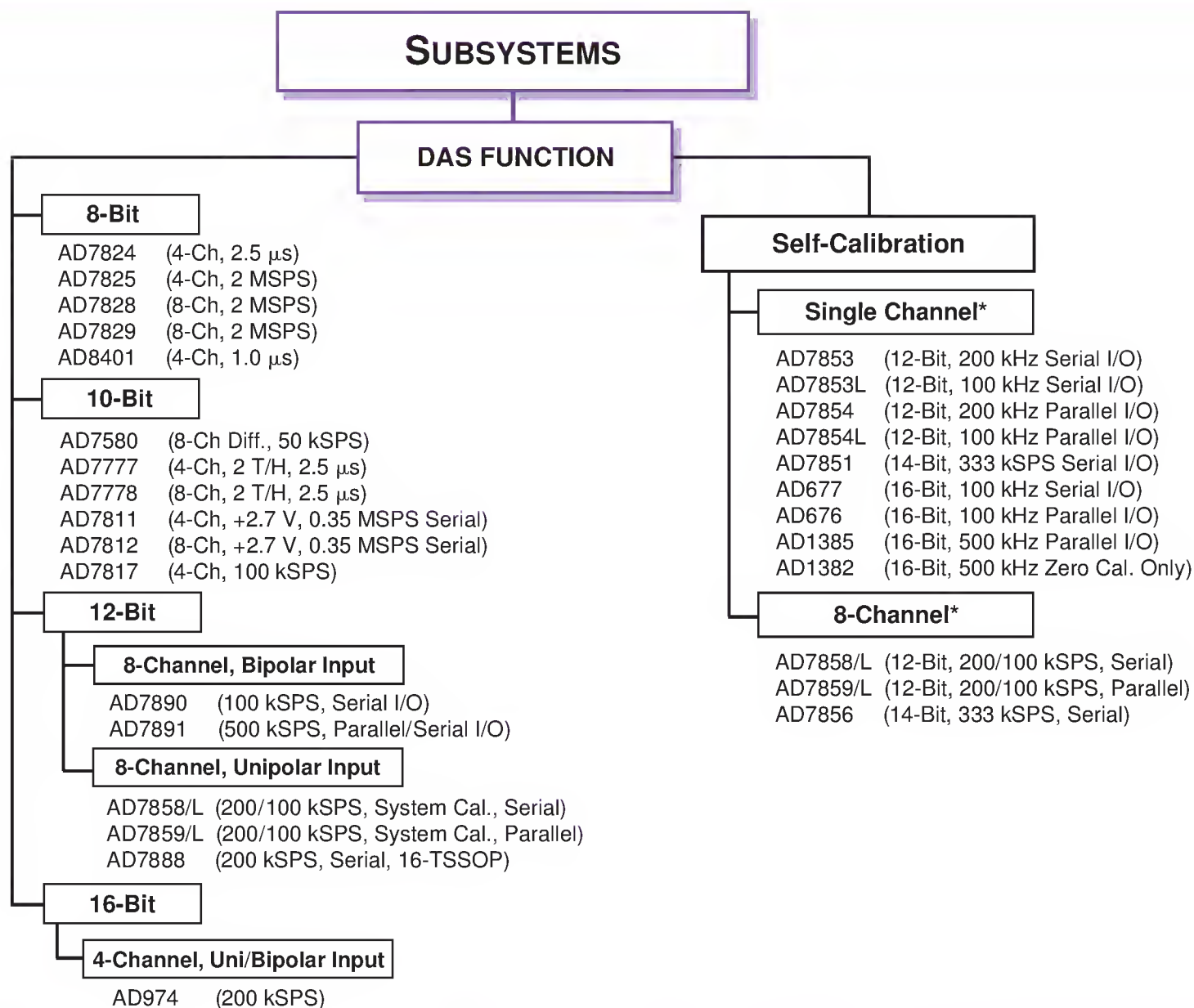


A/D CONVERTERS



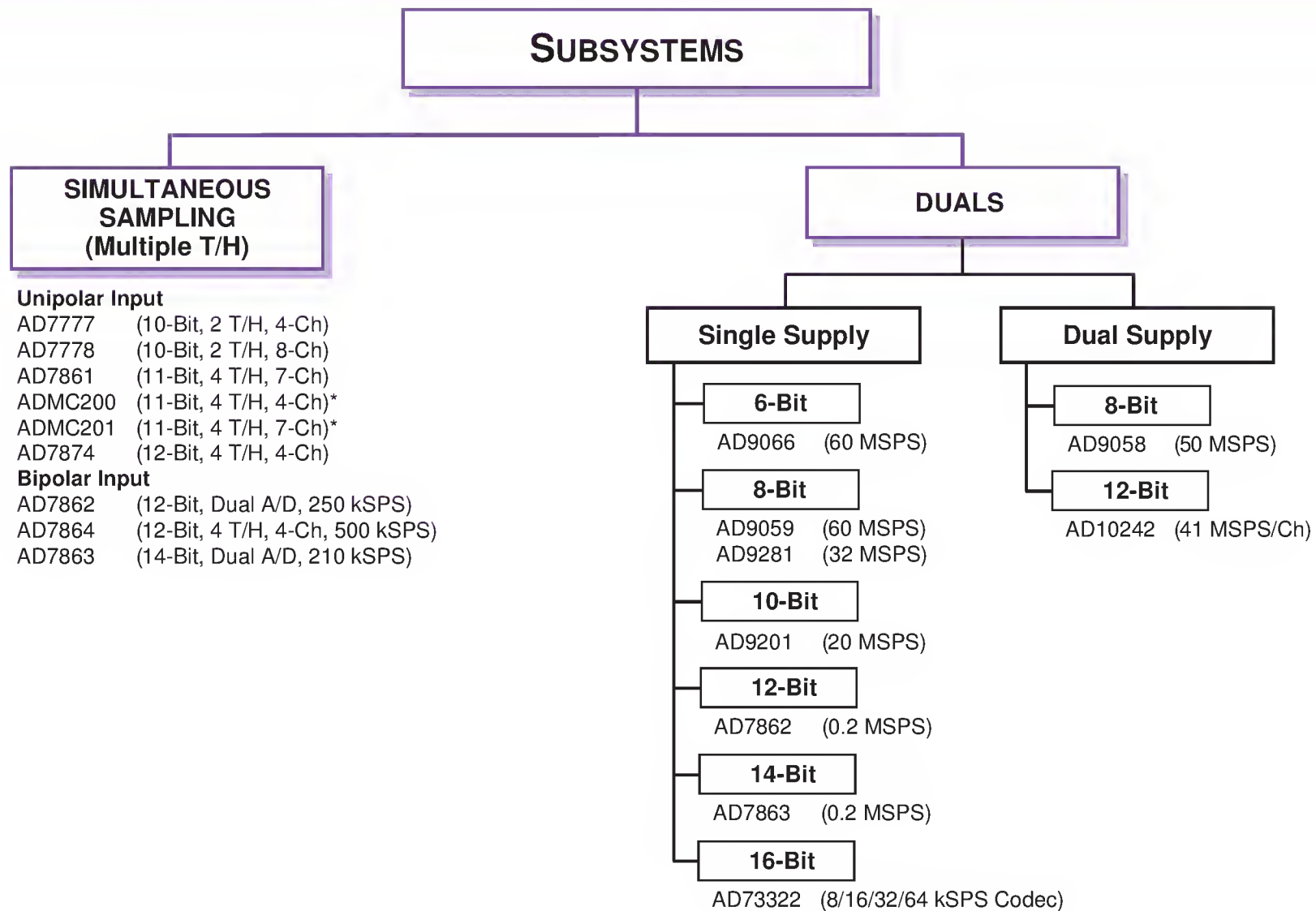
*Charge Balance

A/D CONVERTERS



*L Version +3.0 V Operation.

A/D CONVERTERS



*L Version +3.0 V Operation.

A/D CONVERTERS

CCD SIGNAL PROCESSORS

1-Channel

AD9801 (10-Bit, 18 MSPS)

AD9802 (10-Bit, 18 MSPS)

3-Channel

AD9805/9807 (10/12-Bit, 6 MSPS
w/Triple Correlated Double Sampler,
Digital Offset and Gain Control,
PGA Gain 1 to 4, 16 Steps)

CODEC and I/O PORTS

8-Bit

High Speed, 8-Bit A/D

AD7339 (Dual, 8-Bit D/A, Parallel I/O)

(Dual, 8-Bit D/A, Serial I/O)

500 kSPS

AD7569

AD7669 (Dual, 8-Bit D/A)

AD7769 (Dual, 8-Bit D/A)

12-Bit

AD7729 (270 kSPS, Dual A/D, 1 10-Bit D/A)

AD7868 (83 kSPS, 1 \times A/D, 1 \times D/A, Serial)

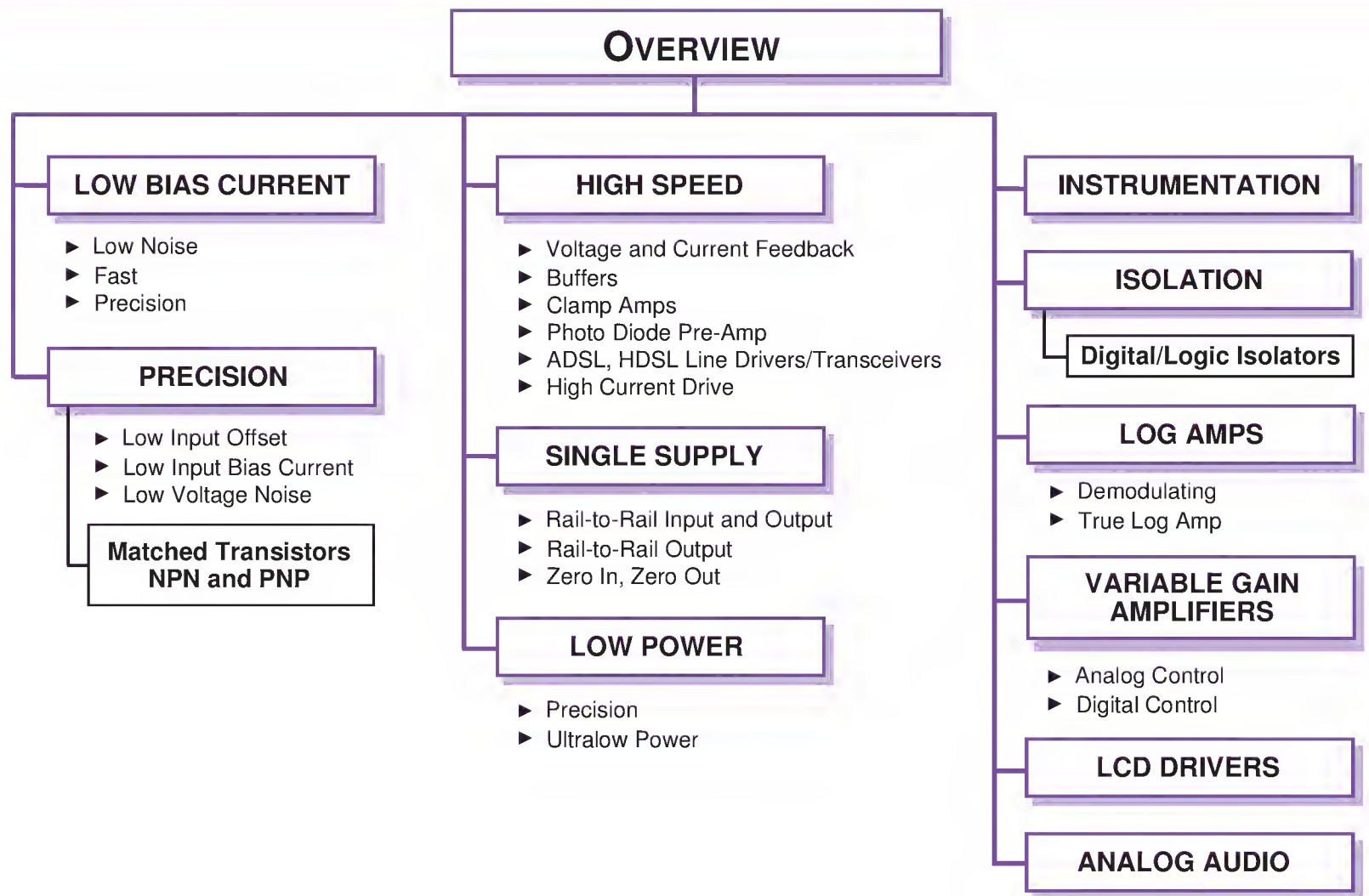
14-Bit

AD7868 (83 kSPS, 1 \times A/D, 1 \times D/A, Serial)

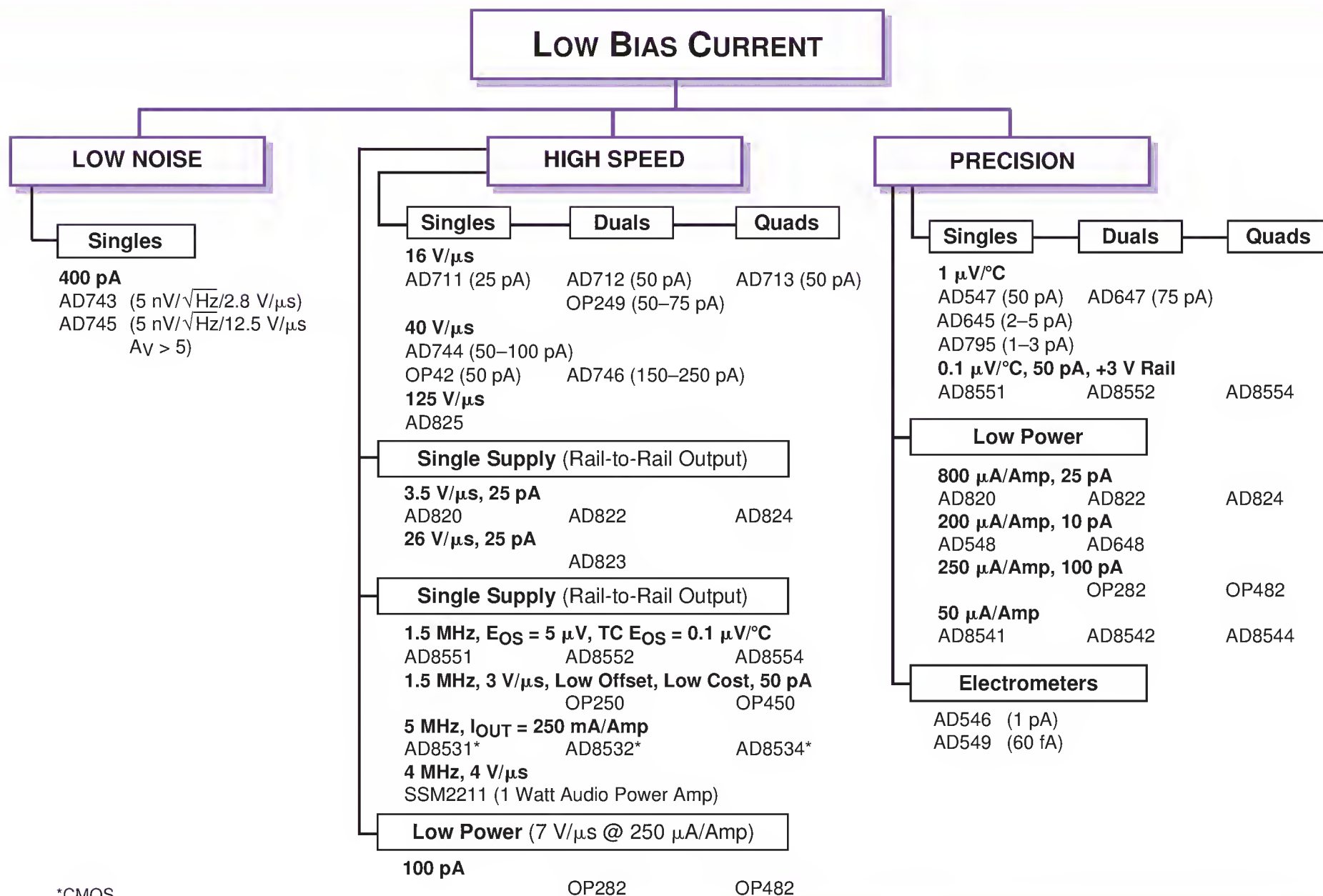
16-Bit

AD73311 (64 kSPS, Low Cost, Monaural Codec)

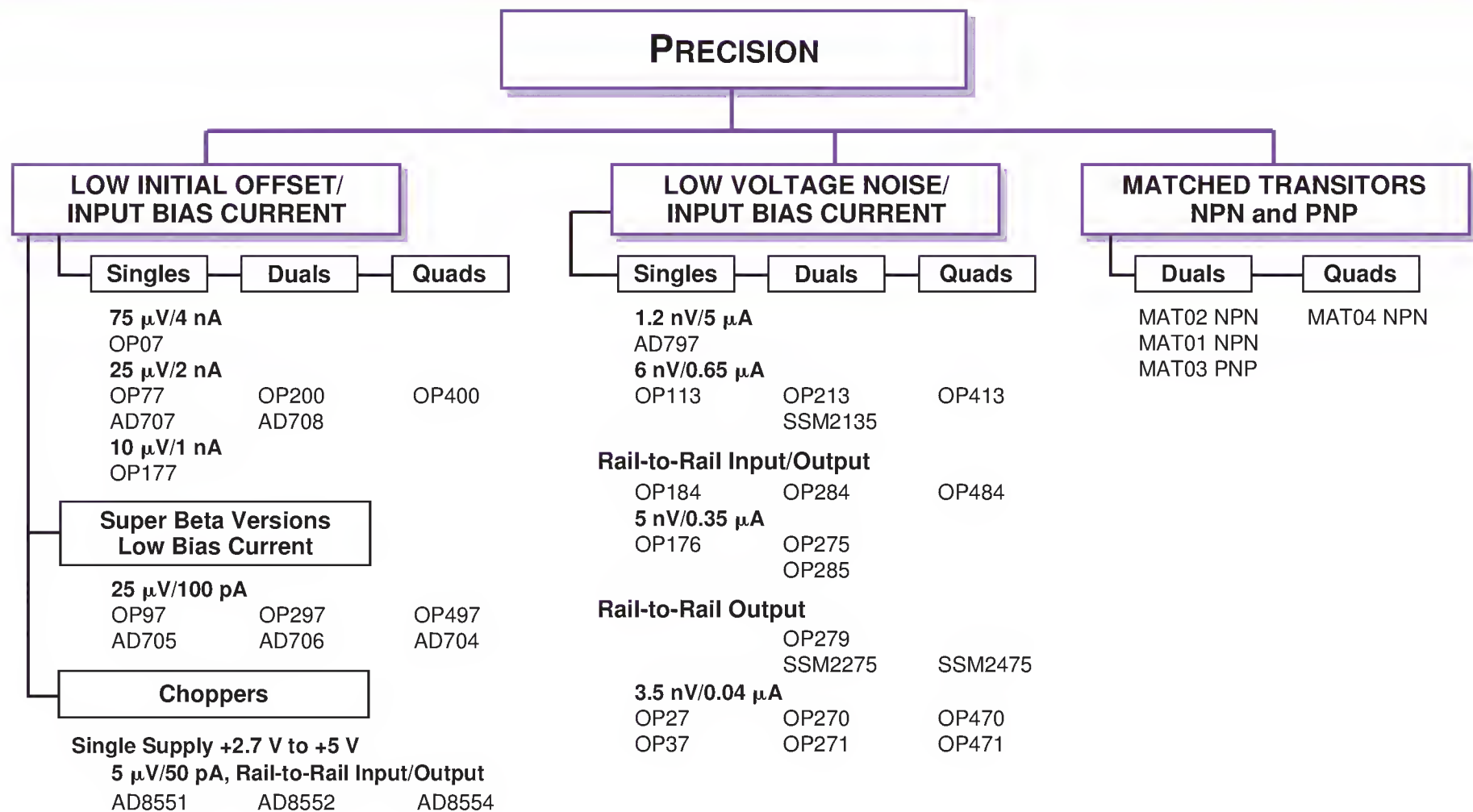
AD73322 (8/16/32/64 kSPS, Stereo Codec)

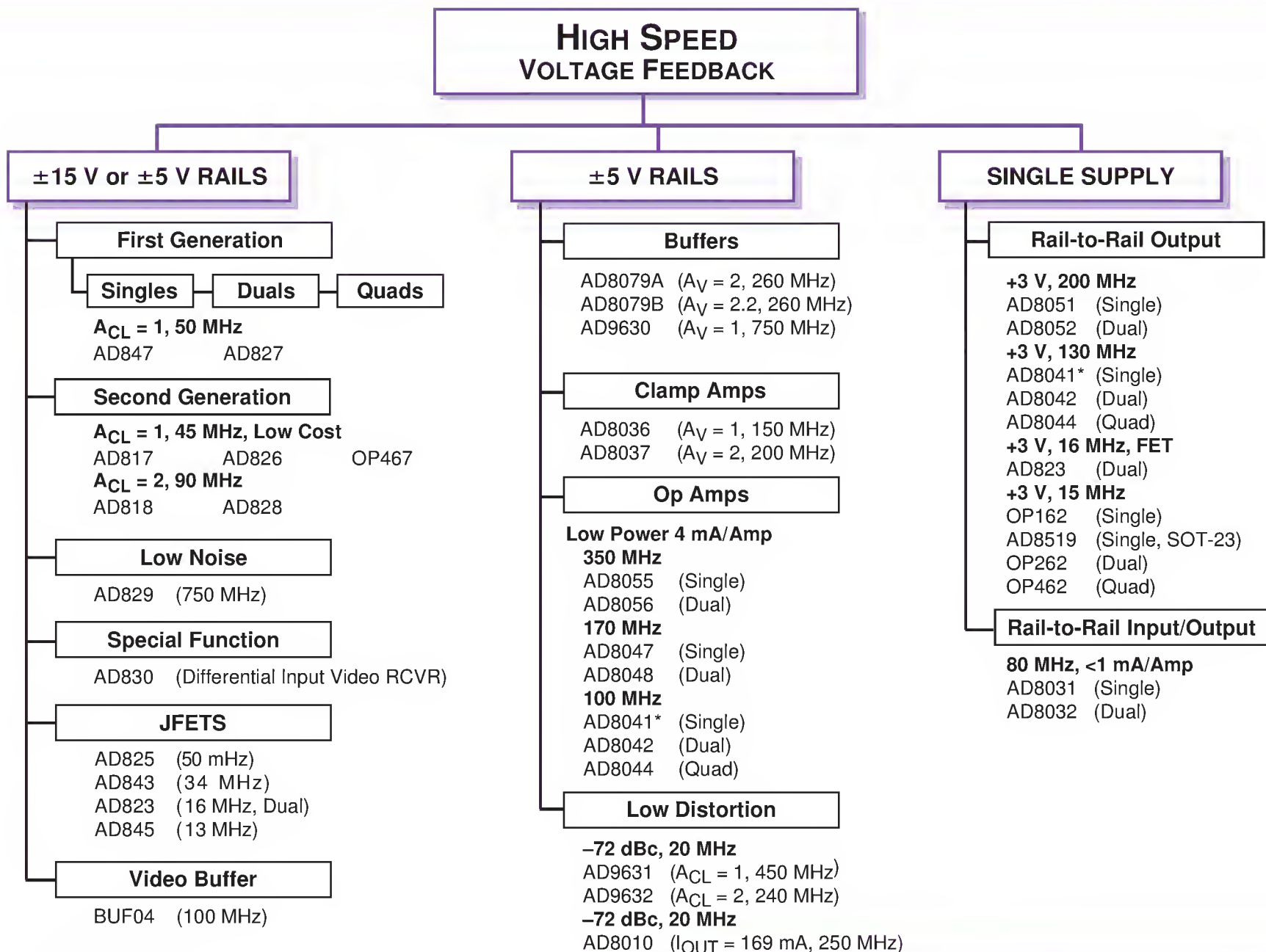


See Analog/Digital Audio Section.



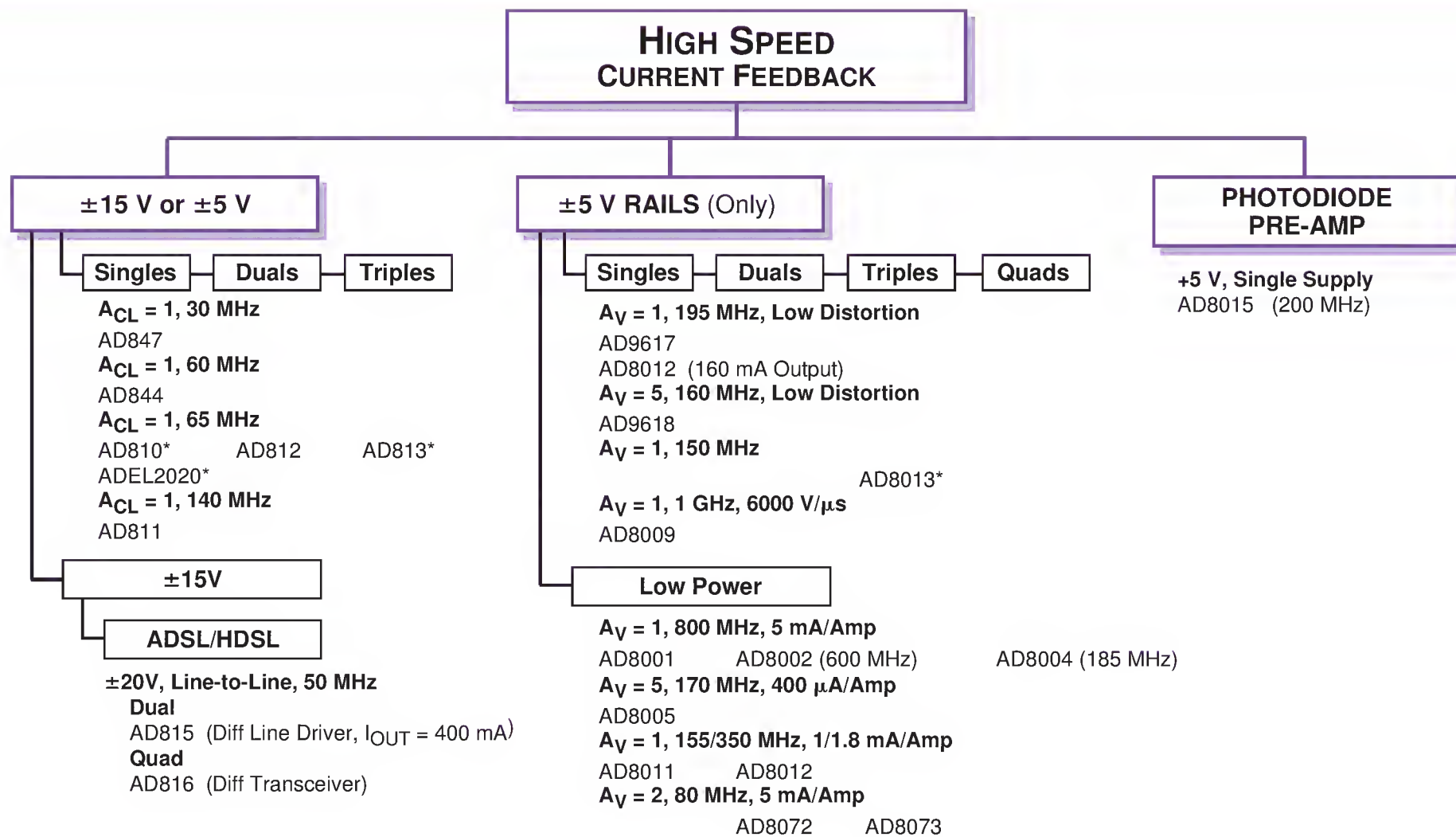
*CMOS



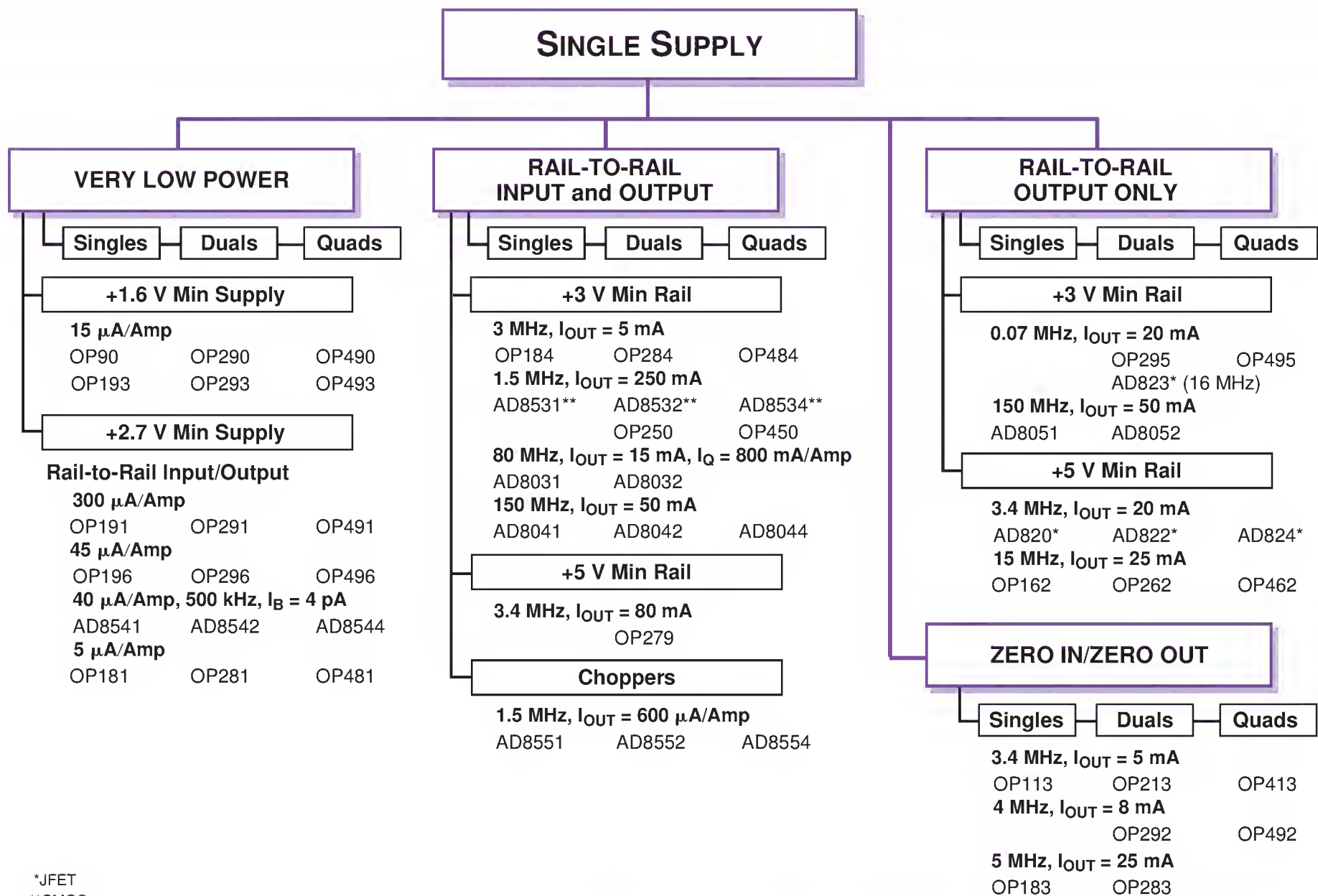


*Output Disable Function

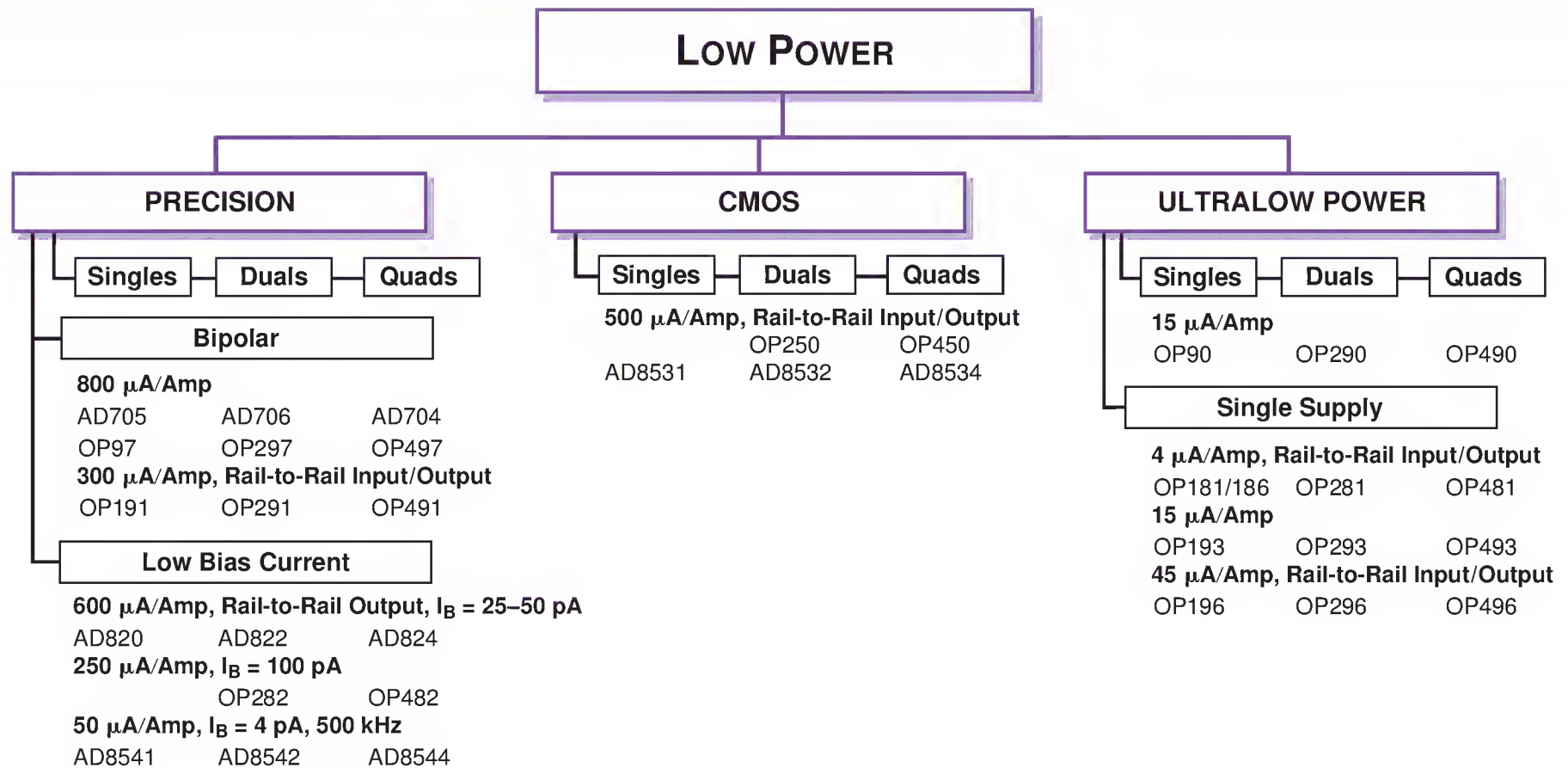
AMPLIFIERS

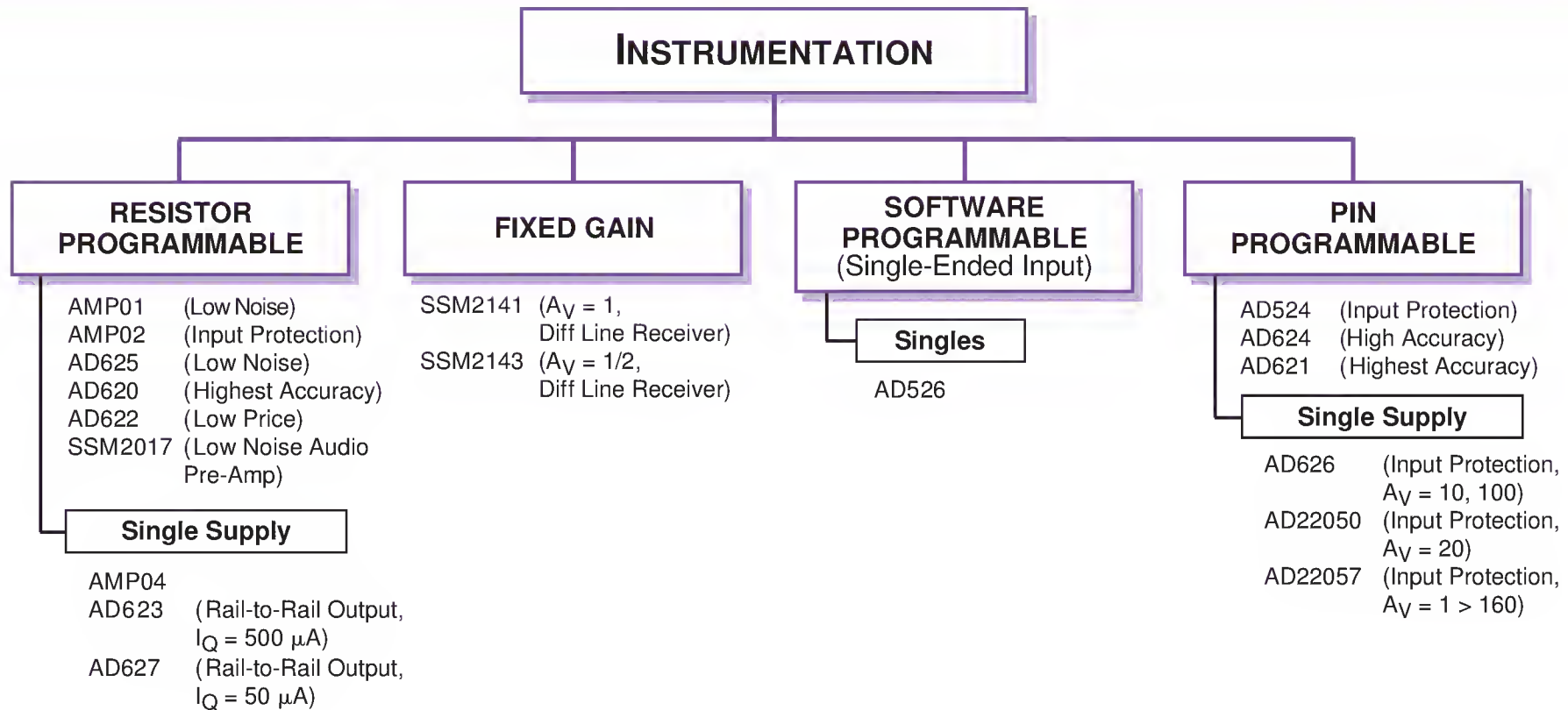


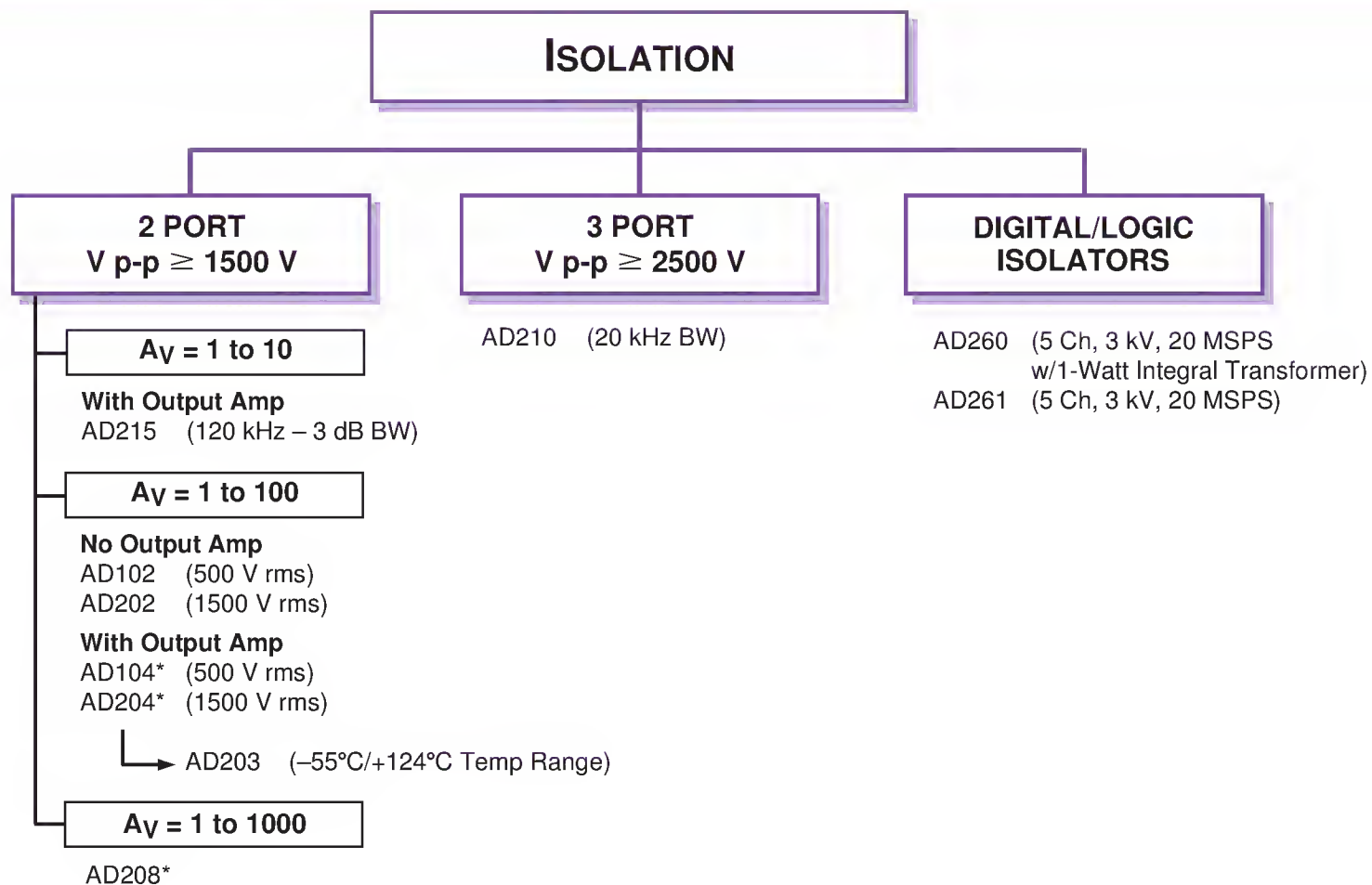
*Output Disable Function



*JFET
 **CMOS







*Multichannel Requires Clock Driver AD246

AMPLIFIERS

LOG AMPS

AD606 (50 MHz, 80 dB, Limiter)
AD640 (120 MHz, 50 dB)
AD8307 (500 MHz, 86 dB)
AD641 (250 MHz, 44 dB)

VARIABLE GAIN

ANALOG CONTROL, AUDIO

140 dB Gain Range
SSM2018T (Voltage Output)
SSM2018T (Current Output)
120 dB Gain Range
SSM2164 (Quad)

DIGITAL CONTROL, VIDEO

+5 V Supply
AD8320 (8-Bit, 200 MHz, 36 dB)

ANALOG CONTROL, VIDEO

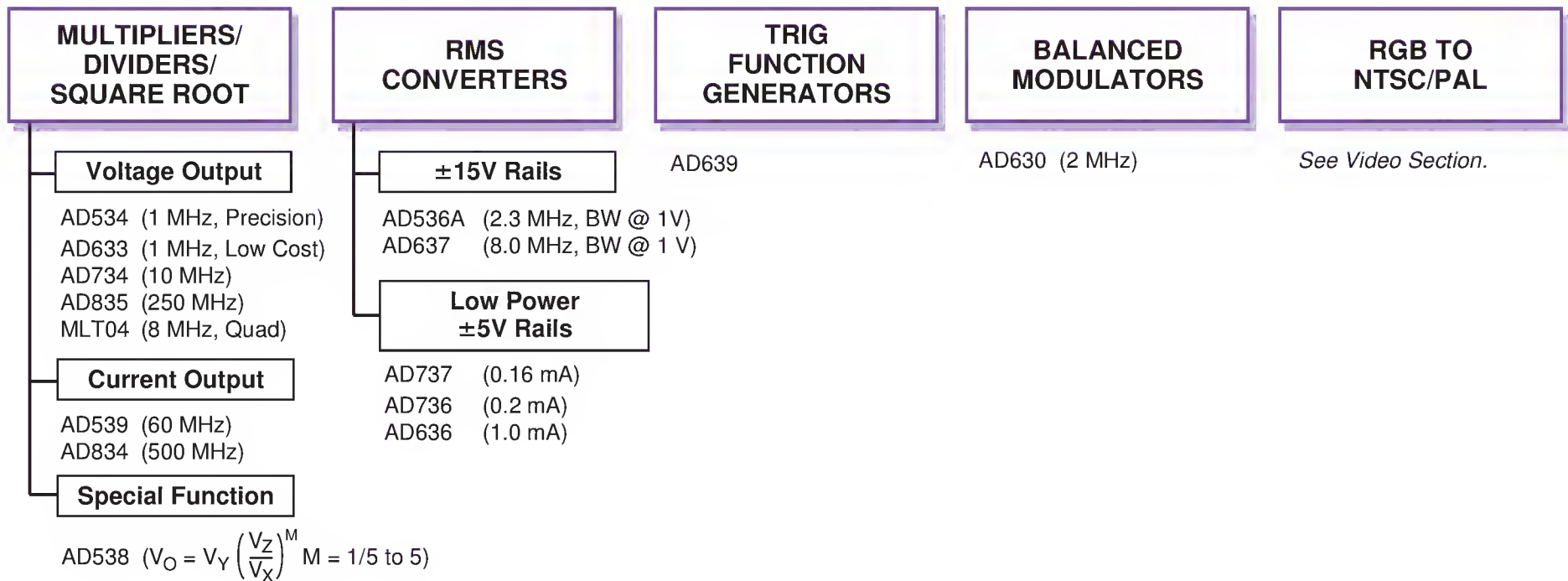
±15 V Supplies
AD600 (35 MHz, Dual, 0 to +40 dB)
AD602 (35 MHz, Dual, -10 to +30 dB)
AD603 (115 MHz, -10 to +30 dB)
AD604 (50 MHz, Dual, w/Pre/Amp)
+5 V Single Supply
AD605 (45 MHz, +48 dB/Channel)

LCD DRIVERS

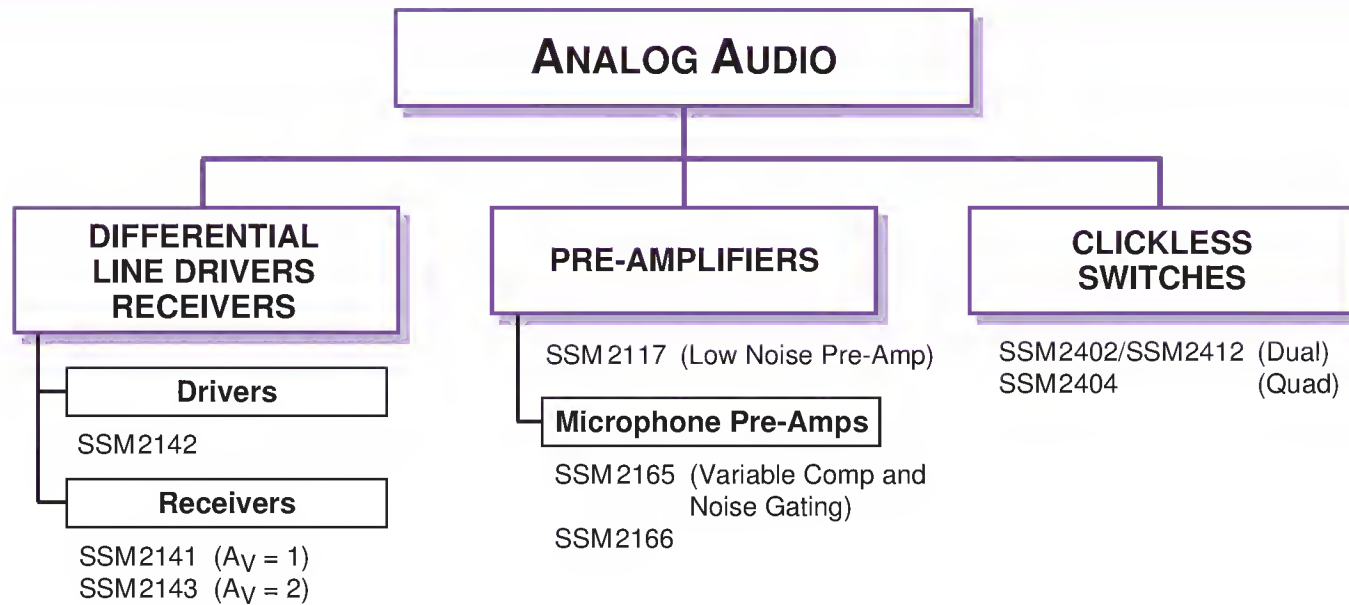
+3.3 V, 2 Muxed Input Channels/Drivers

AD8509 (9 Drivers, $I_{OUT} = 20 \text{ mA/Ch}$)
AD8511 (11 Drivers, $I_{OUT} = 20 \text{ mA/Ch}$)

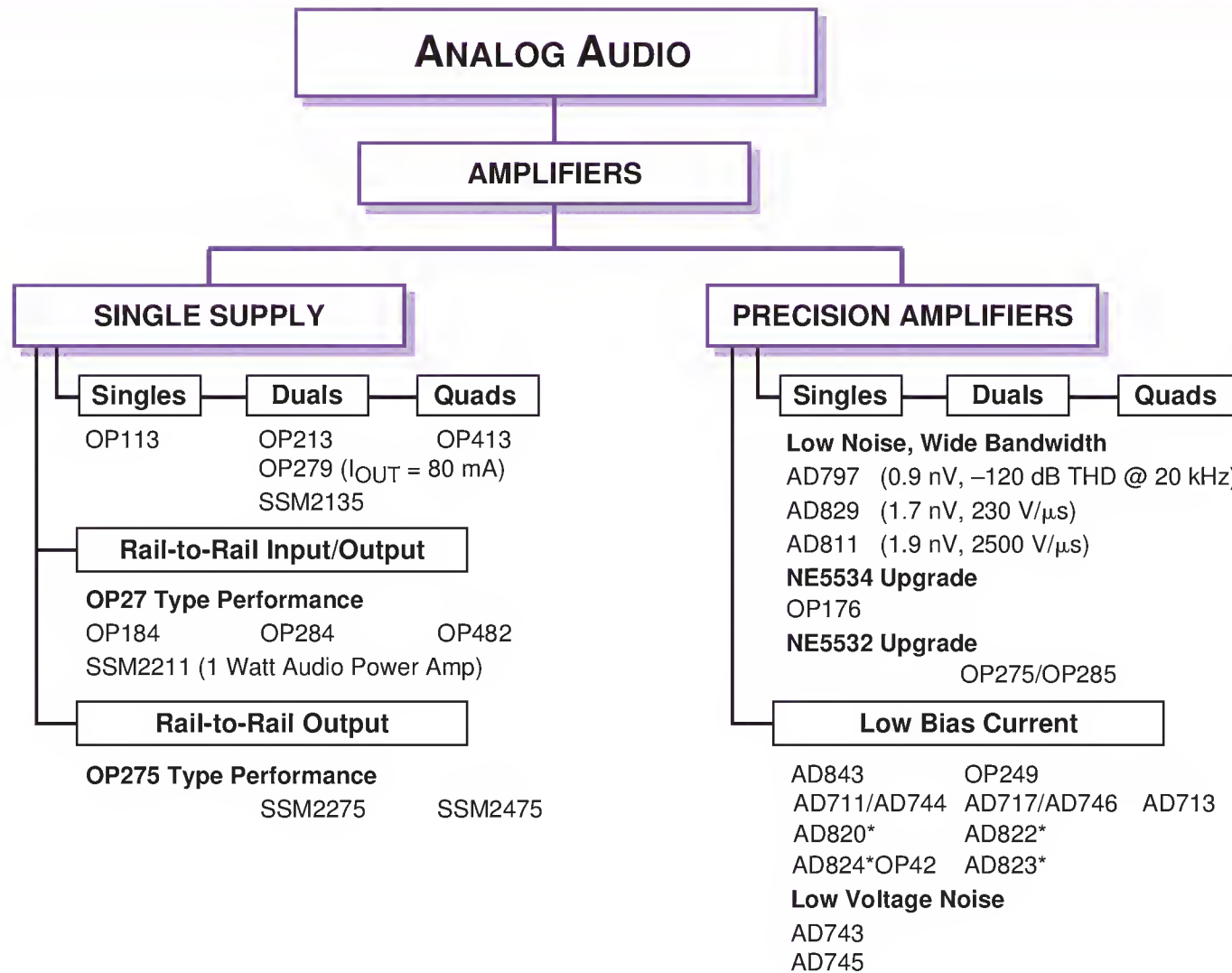
ANALOG COMPUTATIONAL CIRCUITS



ANALOG/DIGITAL AUDIO

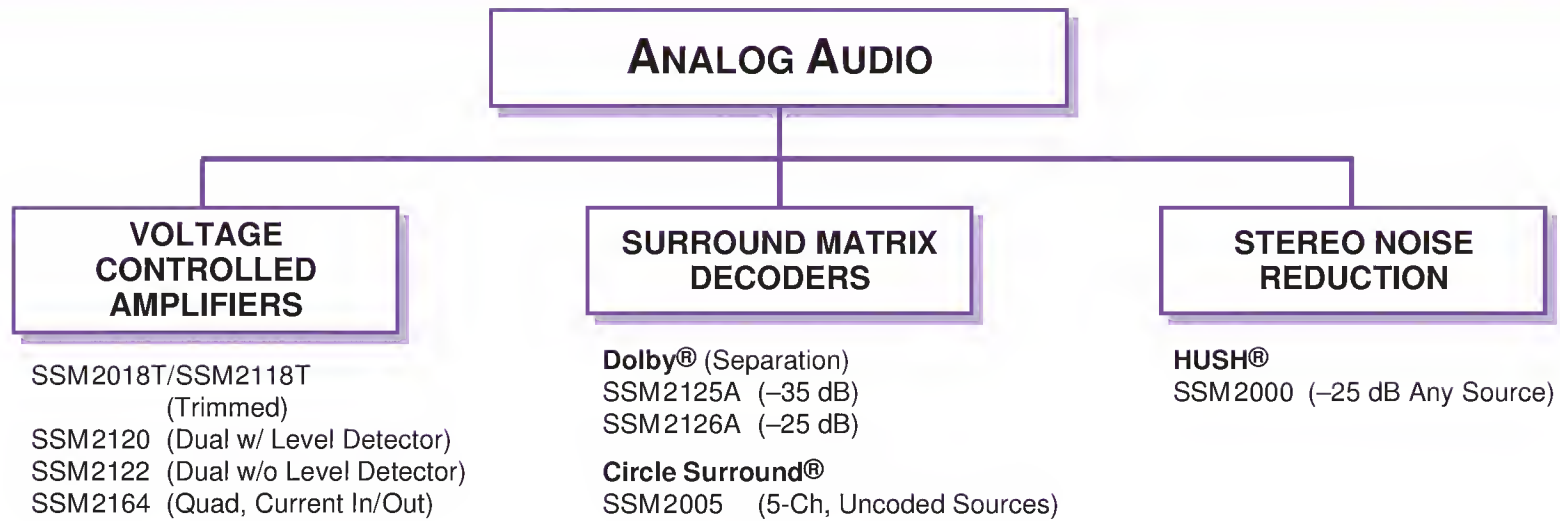


ANALOG/DIGITAL AUDIO



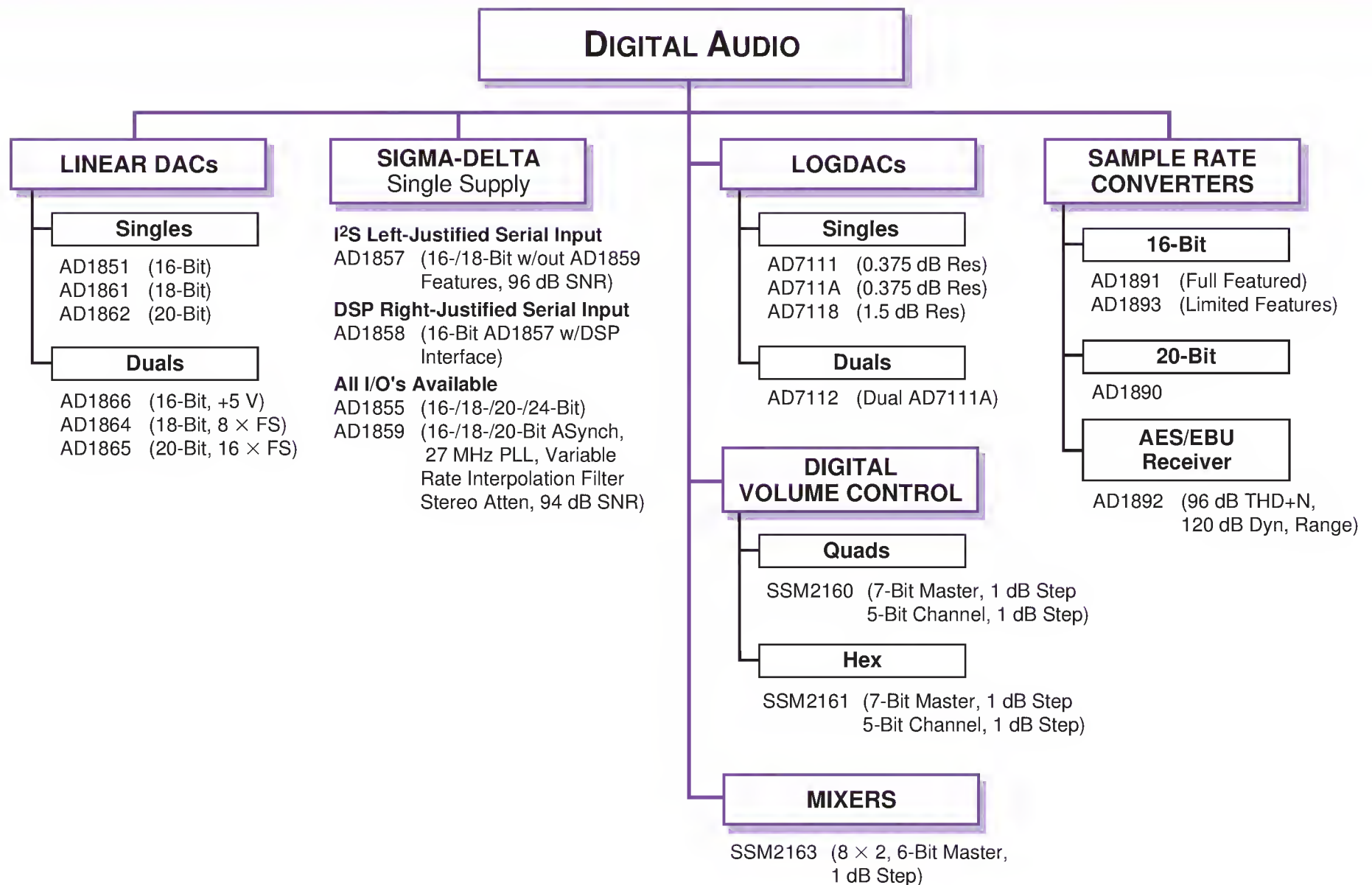
*Rail-to-Rail Output.

ANALOG/DIGITAL AUDIO

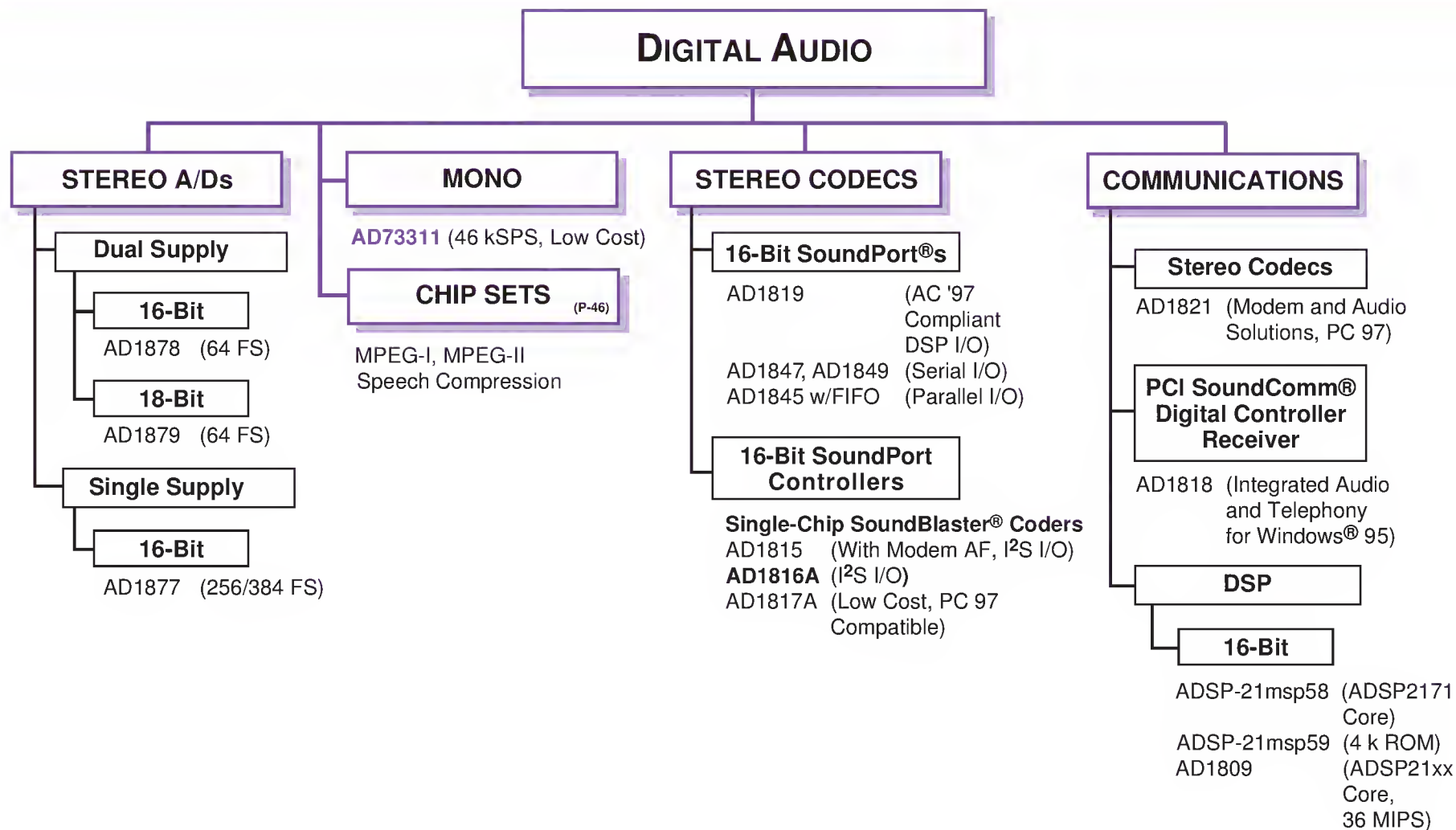


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Circle Surround is a Registered Trademark of Rocktron Corp.
HUSH is a Registered Trademark of Hush Systems.

ANALOG/DIGITAL AUDIO

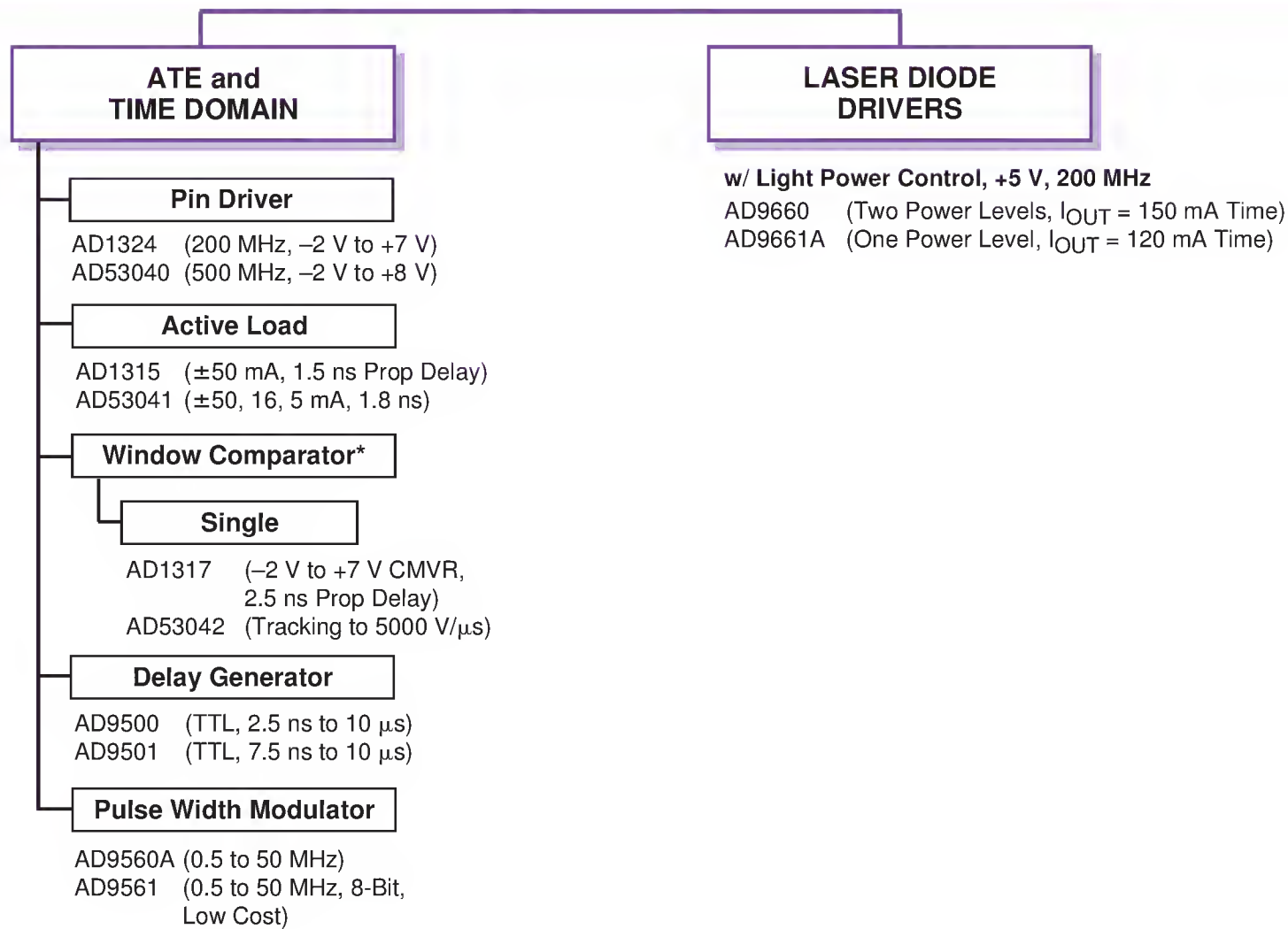


ANALOG/DIGITAL AUDIO

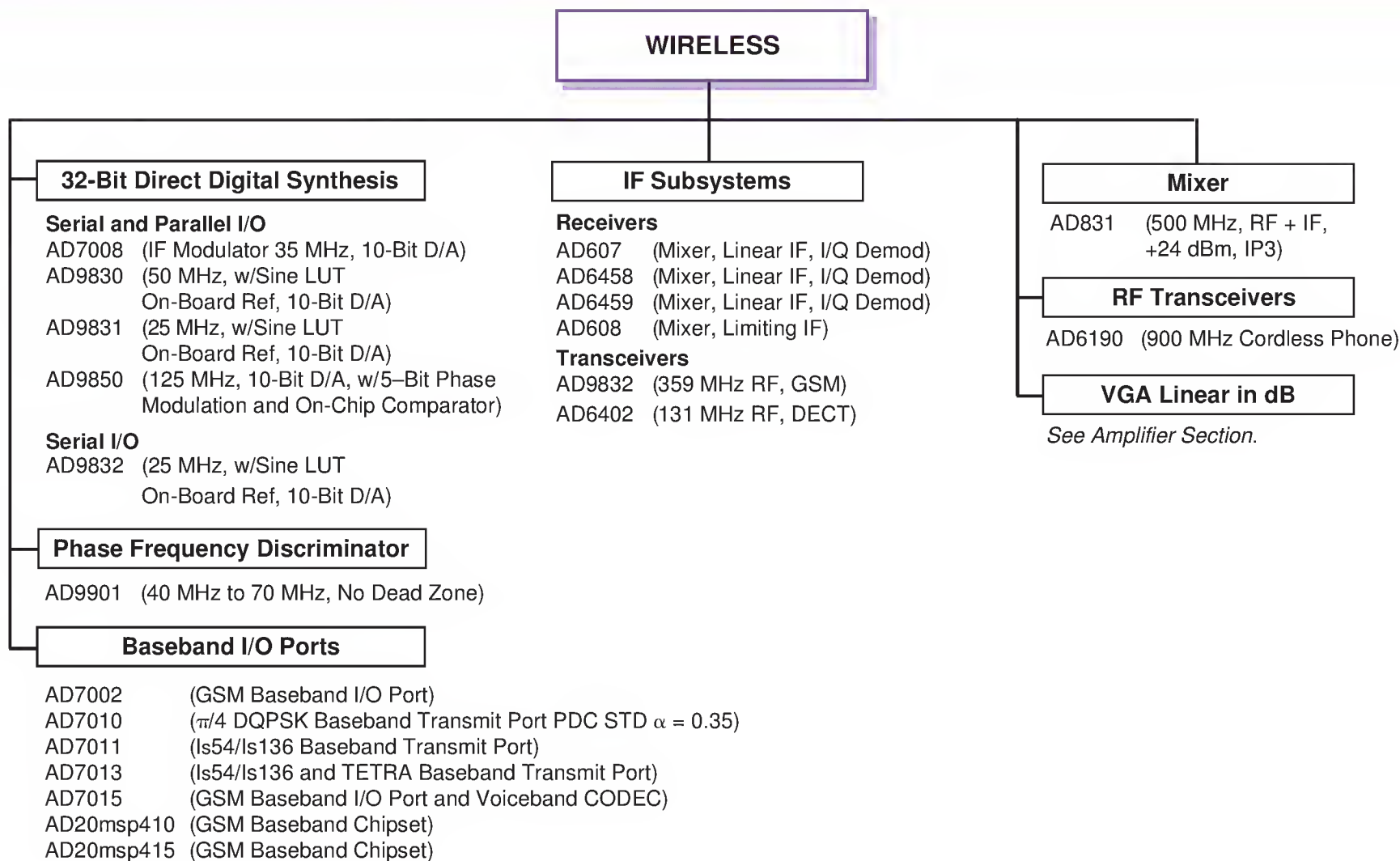


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All other trademarks are the property of their respective holders.

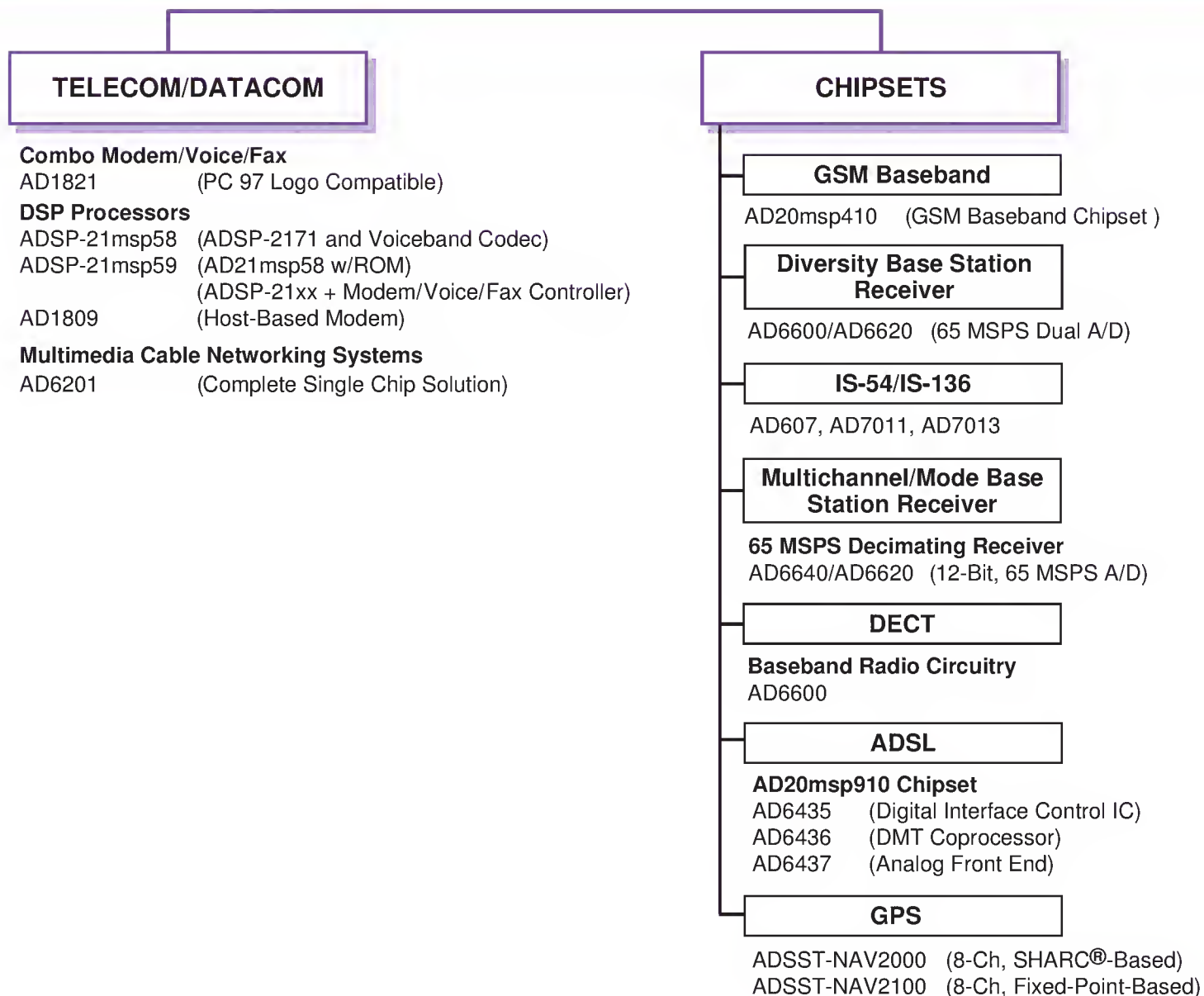
ATE, TIME DOMAIN & LASER DIODE DRIVERS



*For Precision Comparators See Page 33.

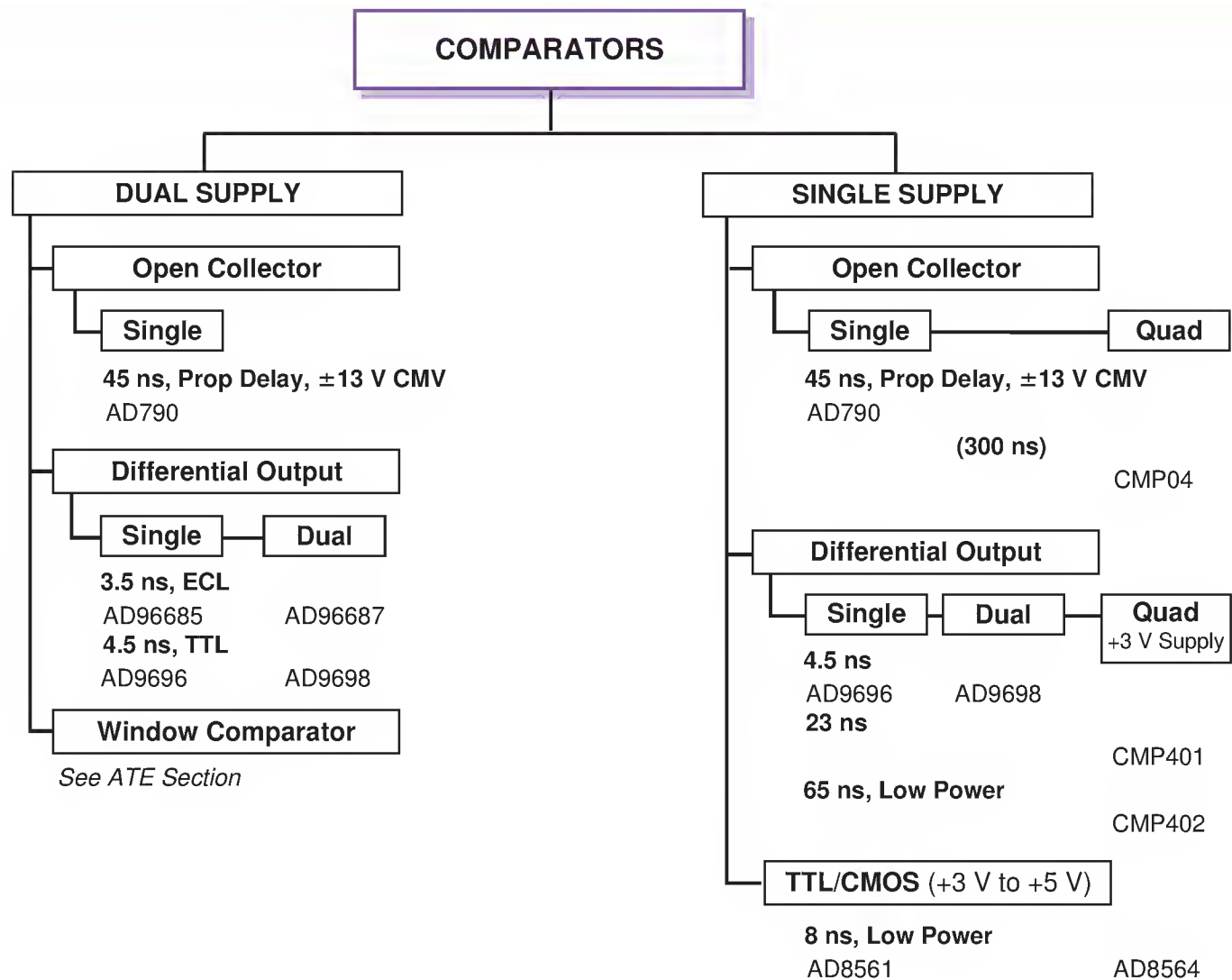


COMMUNICATIONS

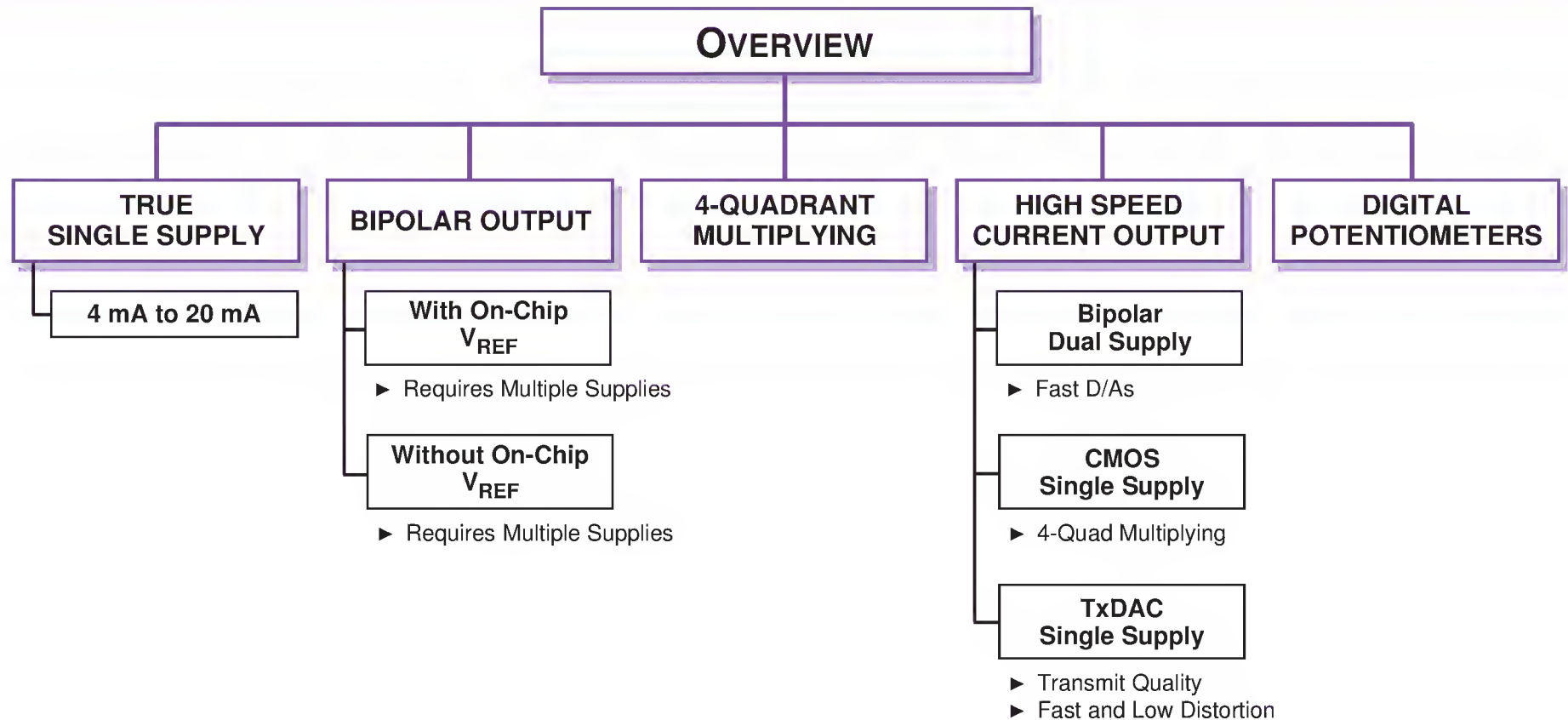


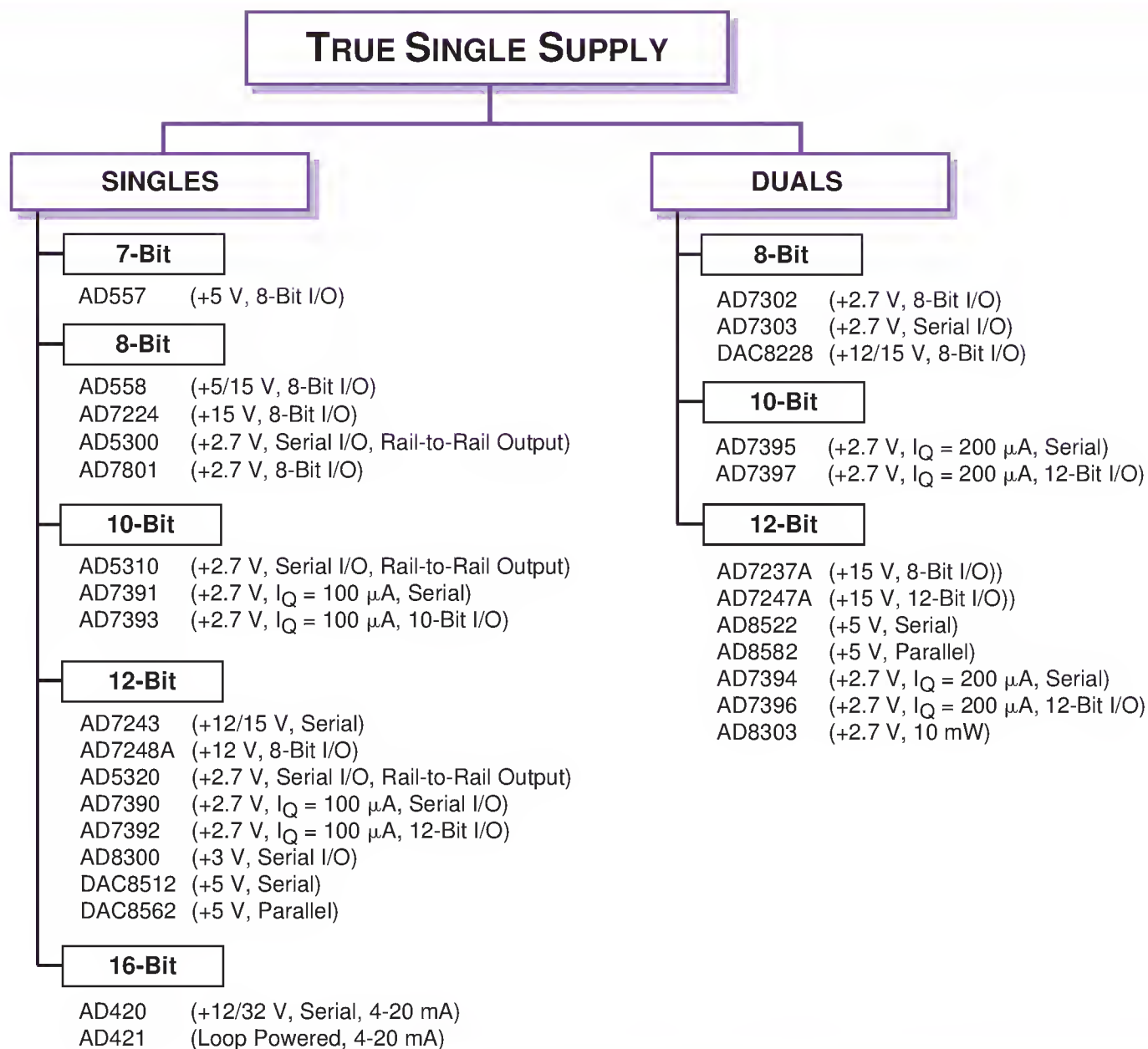
SHARC is a Registered Trademark of Analog Devices, Inc.

COMPARATORS

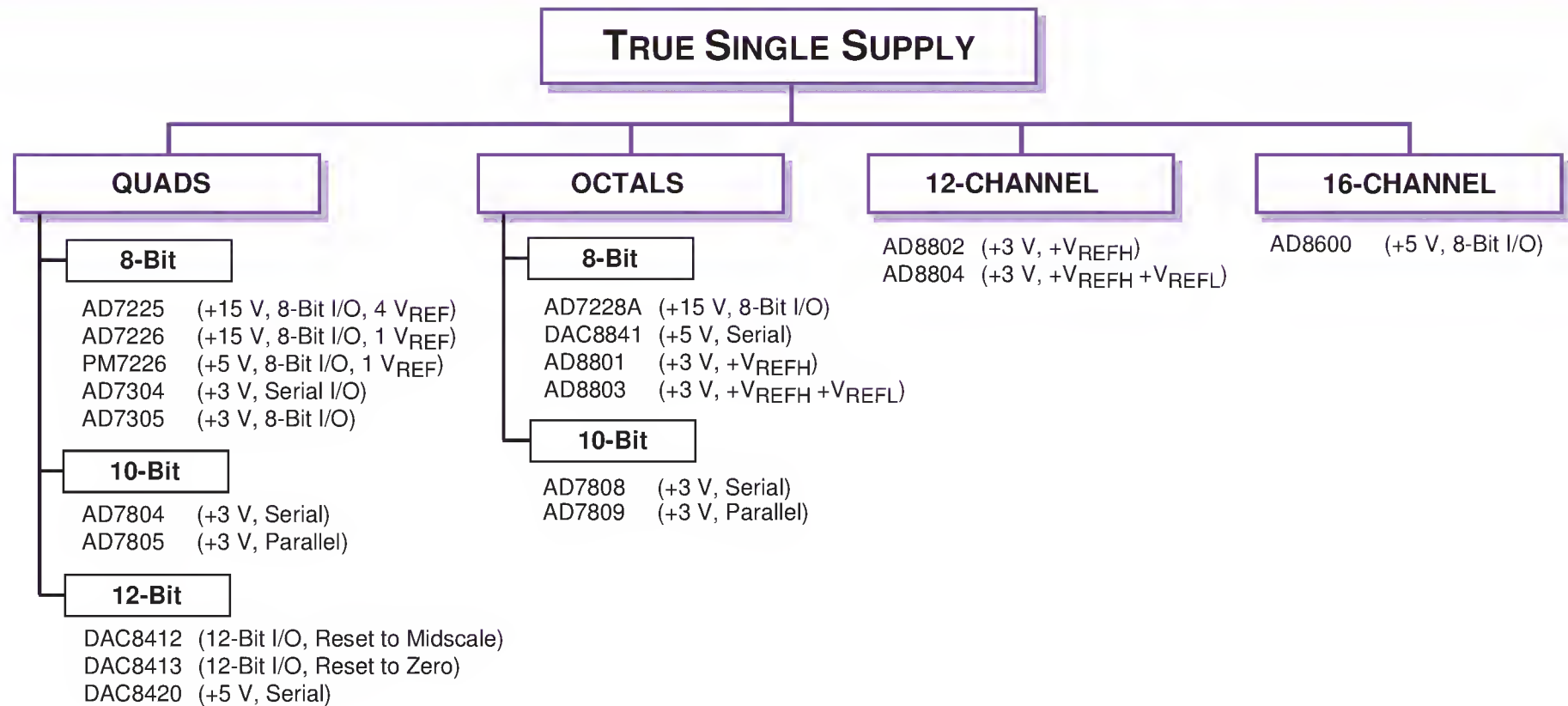


D/A CONVERTERS

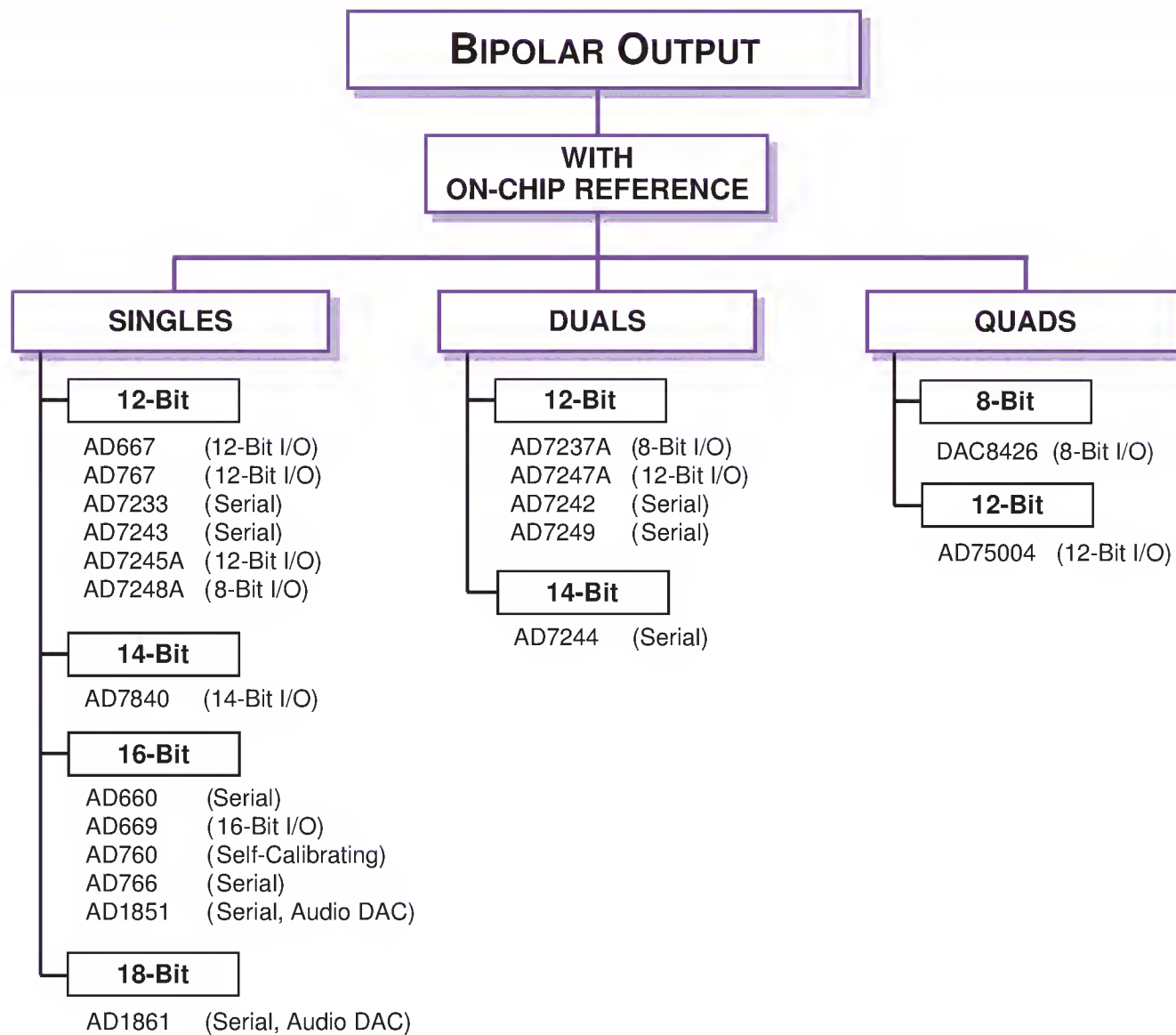




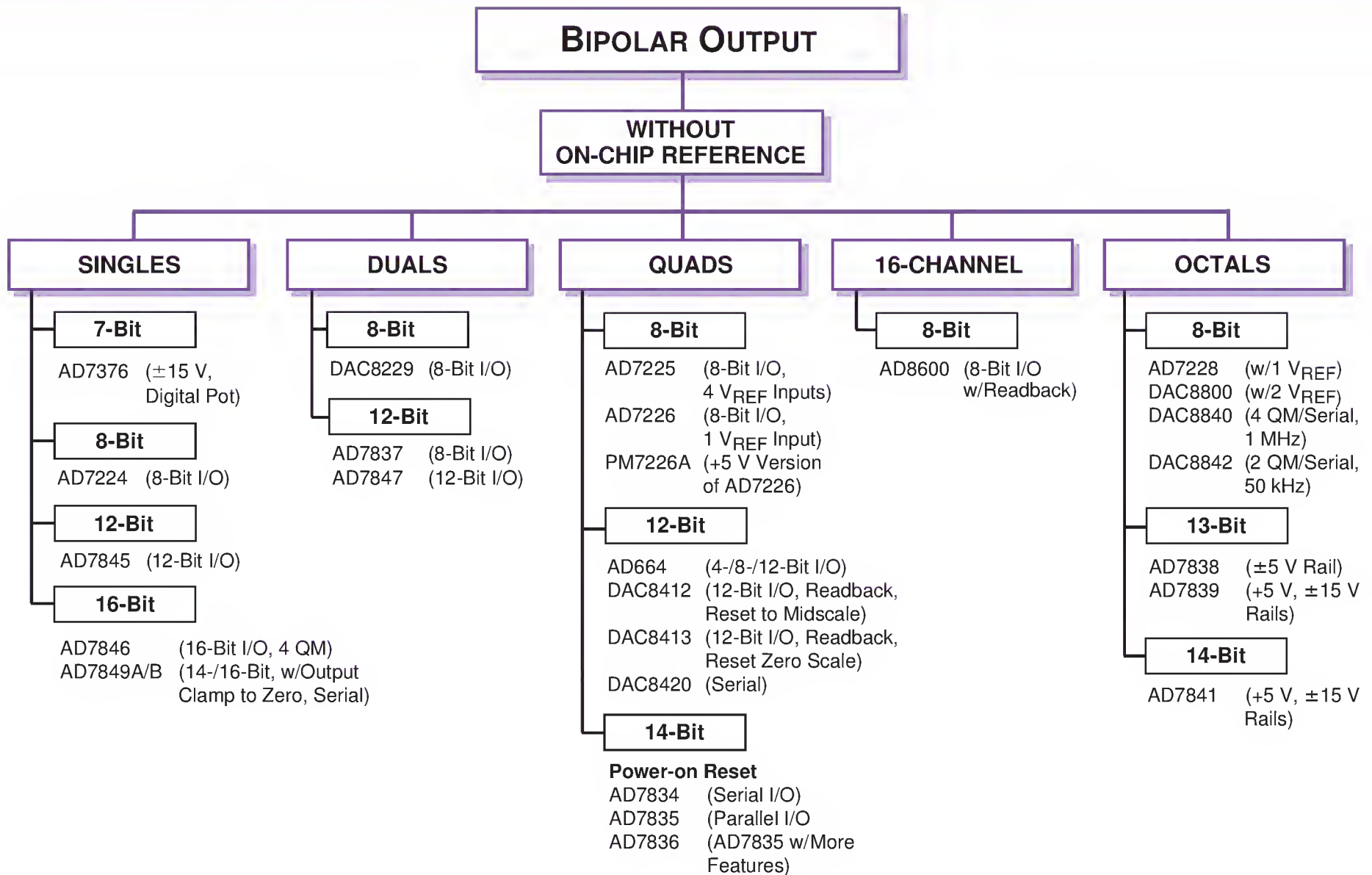
D/A CONVERTERS



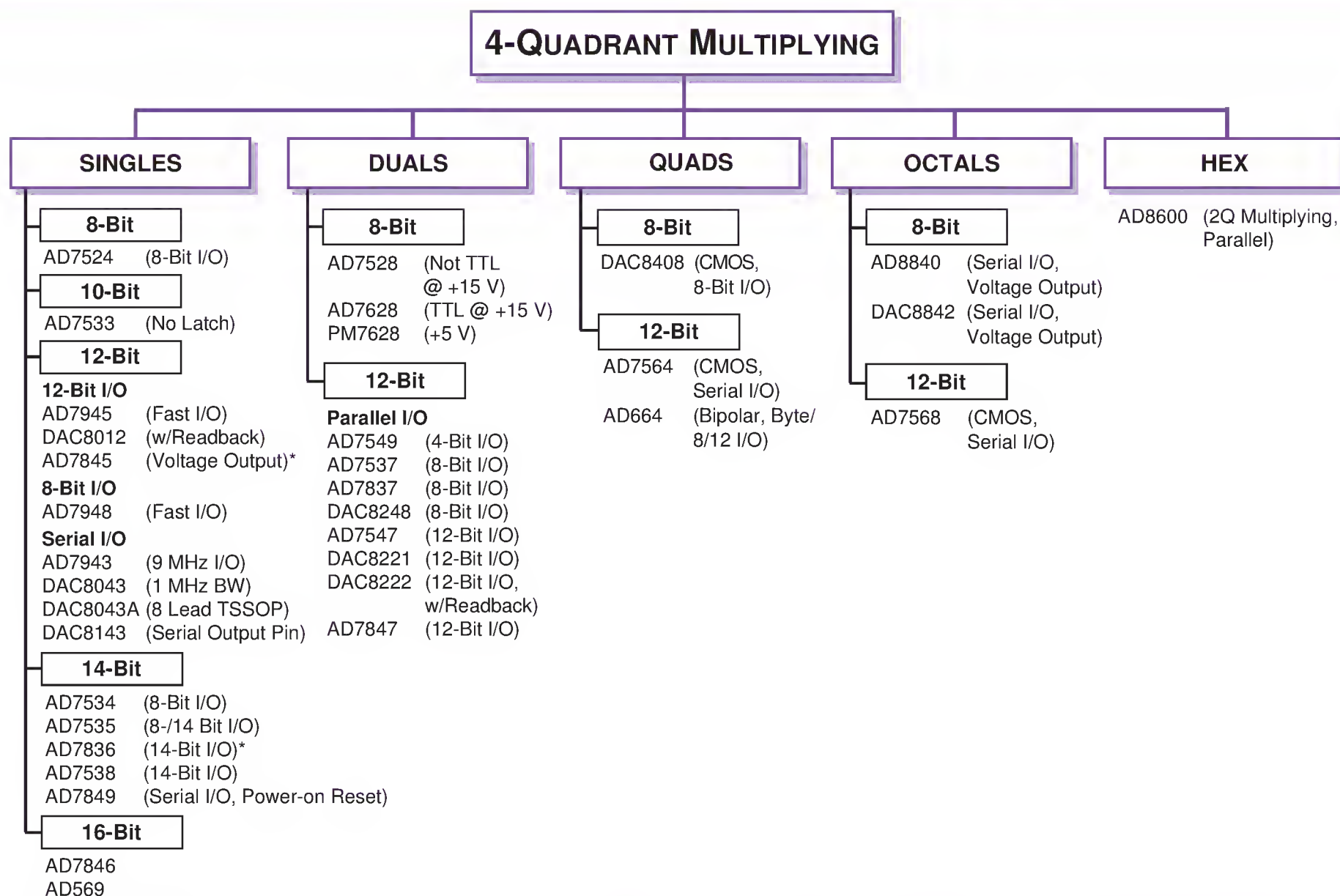
D/A CONVERTERS



D/A CONVERTERS

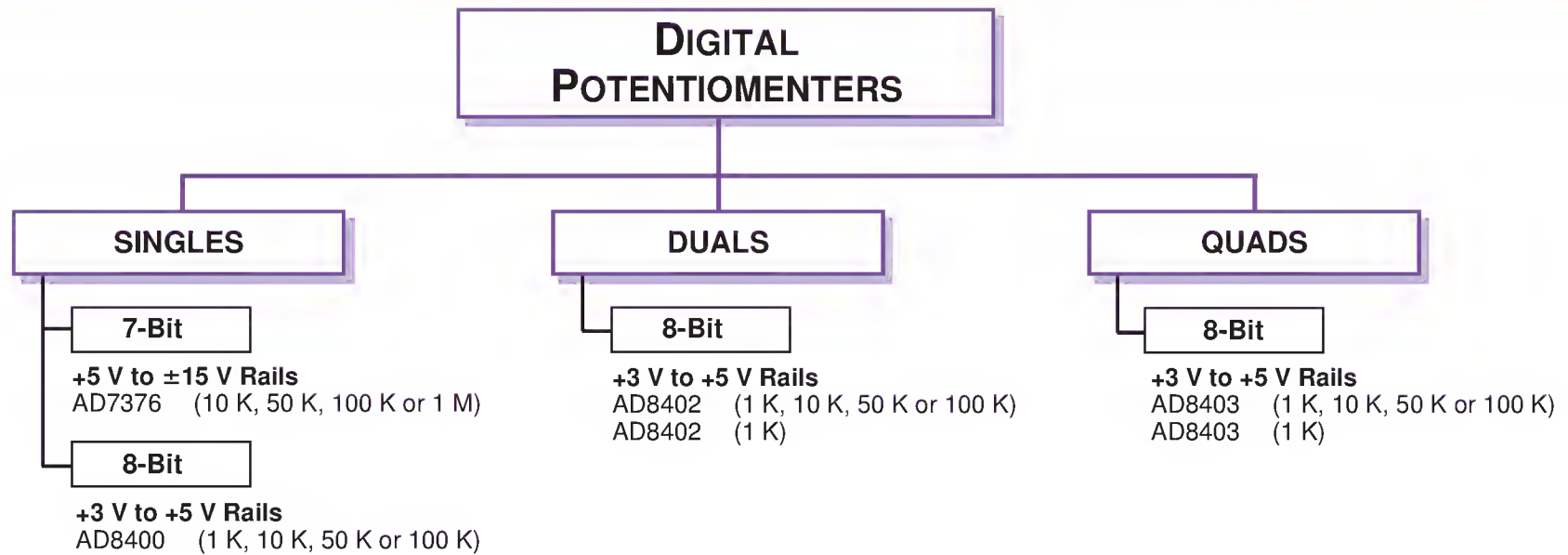


D/A CONVERTERS



*Voltage switching ladder.

D/A CONVERTERS



*All Have Serial I/O

HIGH SPEED and CURRENT OUTPUT TxDACs

BIPOLAR DUAL SUPPLY

8-Bit

AD9701 (250 MSPS)
AD9768 (20 ns)
DAC08 (85 ns)

10-Bit

AD9731 (125 MSPS)

12-Bit

AD568 (35 ns)
AD668 (120 ns)
AD565A (350 ns w/ref)
AD566A (350 ns)
DAC312 (250 ns)

16-Bit

AD569 (4-Quadrant Multiplying)
AD769 (35 MSPS, Low Glitch)
DAC16 (500 ns)

CMOS SINGLE SUPPLY

8-Bit

AD9708 (8-Bit, 100 MSPS TxDAC)

10-Bit

ADV7128 (30/50/80 MHz)
ADV7127 (140 MHz, -48 dB SFDR)
AD9760 (100 MSPS TxDAC)
ADV7128 (50 MSPS TxDAC)

12-Bit

AD9762 (100 MSPS TxDAC)
AD9764 (100 MSPS TxDAC)

14-Bit

AD9774 (128 MSPS TxDAC)

TxDAC Single Supply

Pin-Compatible Transmit Quality D/As

AD9708 (8-Bit, 100 MSPS TxDAC)
AD9760 (10-Bit, 100 MSPS TxDAC)
AD9760-50 (10-Bit, 50 MSPS TxDAC)
AD9762 (12-Bit, 100 MSPS TxDAC)
AD9764 (14-Bit, 100 MSPS TxDAC)
AD9774 (14-Bit, 128 MSPS TxDAC)

With Interpolation Filters

AD9761 (Dual 10-Bit, 40 MSPS,
2x Interpolation Filter)
AD9774 (14-Bit, 32 MSPS, 128 MHz,
4x Interpolation Filter)

4-Quadrant Multiplying

12-Bits

AD7943 (Serial I/O, 300 ns)
AD7945 (12-Bit I/O, 300 ns)
AD7948 (8-Bit I/O, 300 ns)

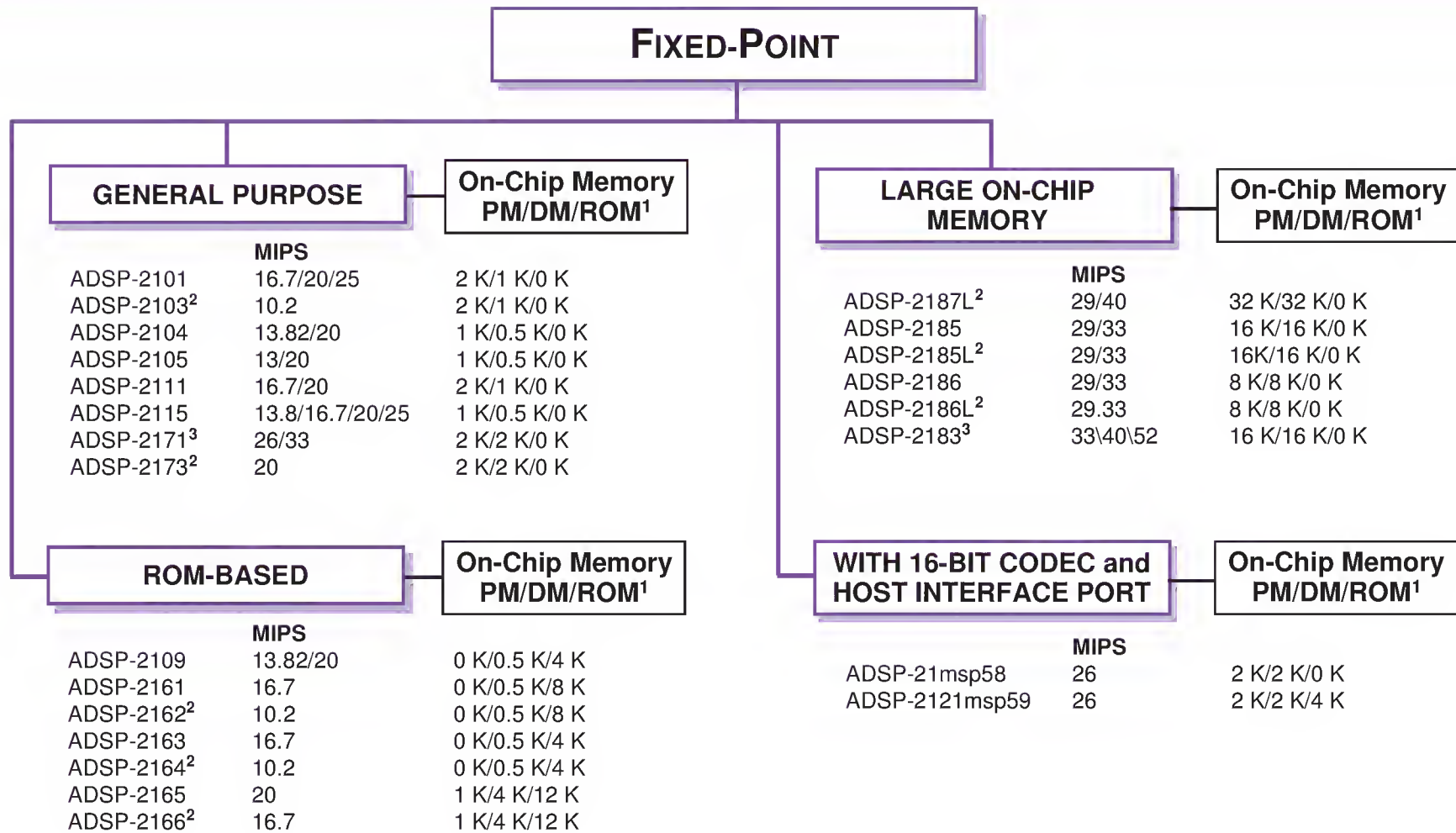
Direct Digital Synthesizers 32-Bit

See Communications Section on Page 31.

Triple 10-Bit

See Video and Multimedia Section

DIGITAL SIGNAL PROCESSING



¹ PM/DM/ROM = Program RAM/Data RAM/Program ROM

² +3.3 V Operation

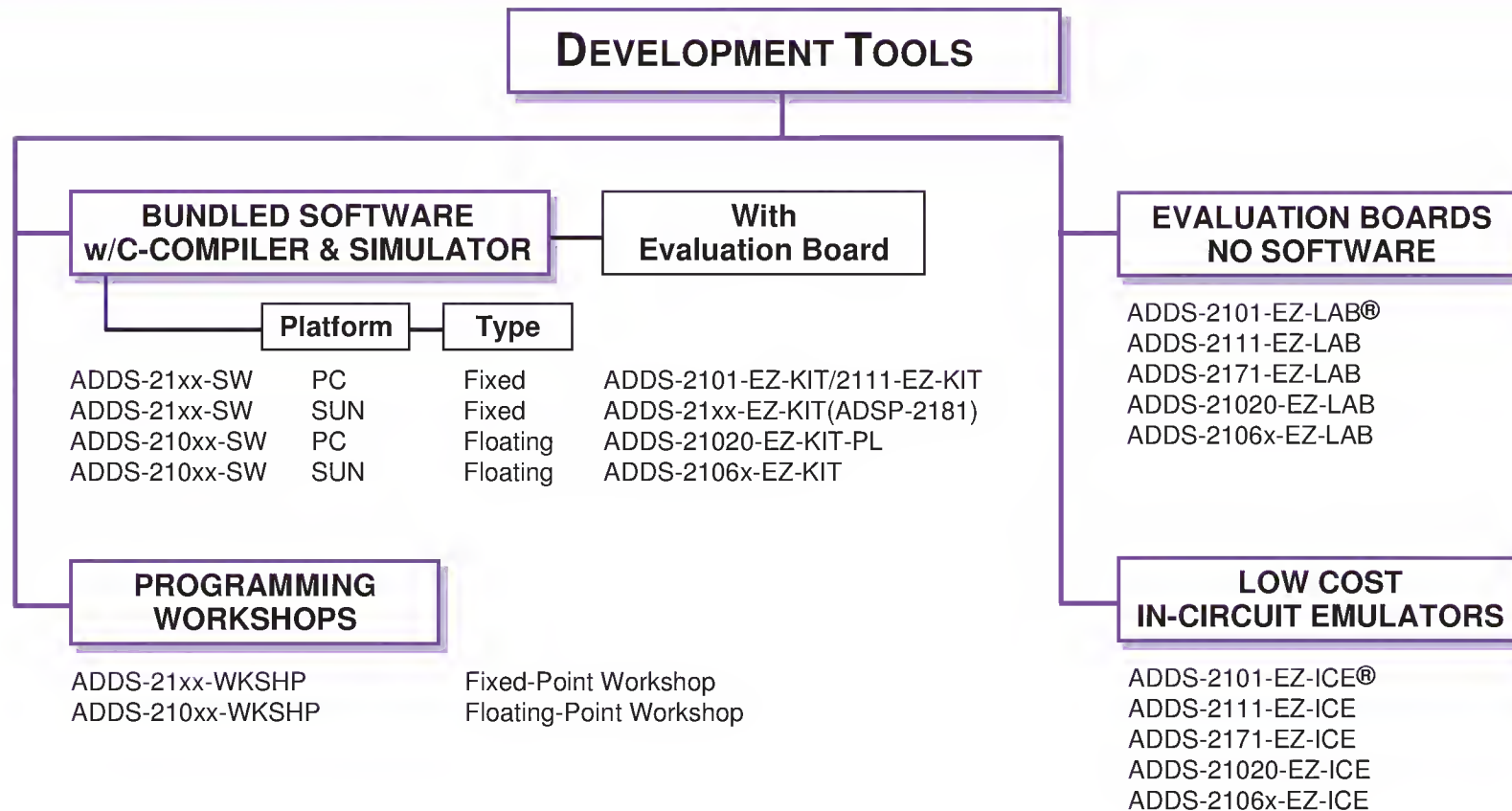
³ Multiple Power-Down Modes

DIGITAL SIGNAL PROCESSING

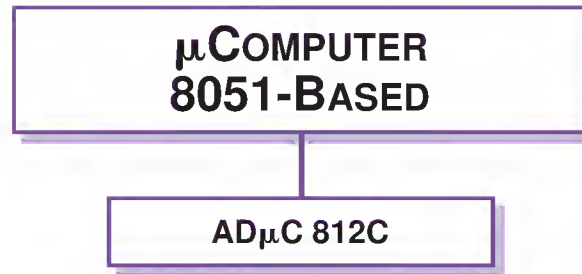
FLOATING-POINT		
	Cycle Time ns	On-Chip Memory PM/DM ¹
ADSP-21020	25/40/50	0 K/0 K
ADSP-21060	25	2 Mbit/2 Mbit
ADSP-21061	25	0.5 Mbit/0.5 Mbit
ADSP-21061L	25	3 V Operation
ADSP-21062	25	1 Mbit/1 Mbit
ADSP-21062L	25	3 V Operation
AD14060	25	8 Mbit/8 Mbit
	QUAD ADSP-21060	On-Chip Memory PM/DM ¹
AD14160	480 MFlops	8 Mbit/8 Mbit
AD14160L	480 MFlops	8 Mbit/8 Mbit, 3 V Operational
AD14061	480 MFlops	8 Mbit/8 Mbit, 452 Lead Ball Grid Array
AD14061L	480 MFlops	8 Mbit/8 Mbit, 452 Lead Ball Grid Array. +3 V

¹PM/DM = Program RAM/Data RAM

DIGITAL SIGNAL PROCESSING



ADSP-21csp01: Compilers/Software/Evaluation Boards and Emulators Are Under Development.
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- ▶ 16 MHz Operation
Analog I/O
8-Ch, 200 kSPS, 12-Bit A/D

DIGITAL SIGNAL PROCESSING

CHIPSETS

GPS

ADSST-NAV2000 (8-Ch, SHARC-Based)
ADSST-NAV2100 (8-Ch, Fixed-Point-Based)

SPEECH COMPRESSION

ADSST-G.723.1
ADSST-G.728
ADSST-G.729

MPEG-I AUDIO

MPEG-I (Encode and Decode)
ADSST-MPEG-100 Series

MPEG-II AUDIO

ISO/IEC 13818-3 (Audio Std)
ADSST-MPEG-2000 (6-Ch Decoder)

ADSL

AD28msp910

NOISE/ECHO CANCELLATION

For Mobile Phones
AD20msp400HF

GSM BASEBAND

AD20msp410 (1st Generation)

REFERENCE DESIGNS

DIGITAL TELEPHONY

ADSST-DAM-1000 Series

VIDEO PHONE H.324 FOR POTS

Host-Based (ADSST-VC-3000)
ADSP21062 DSP-Based (ADSST-VC-2000)

V.34 MODEM DATA PUMP

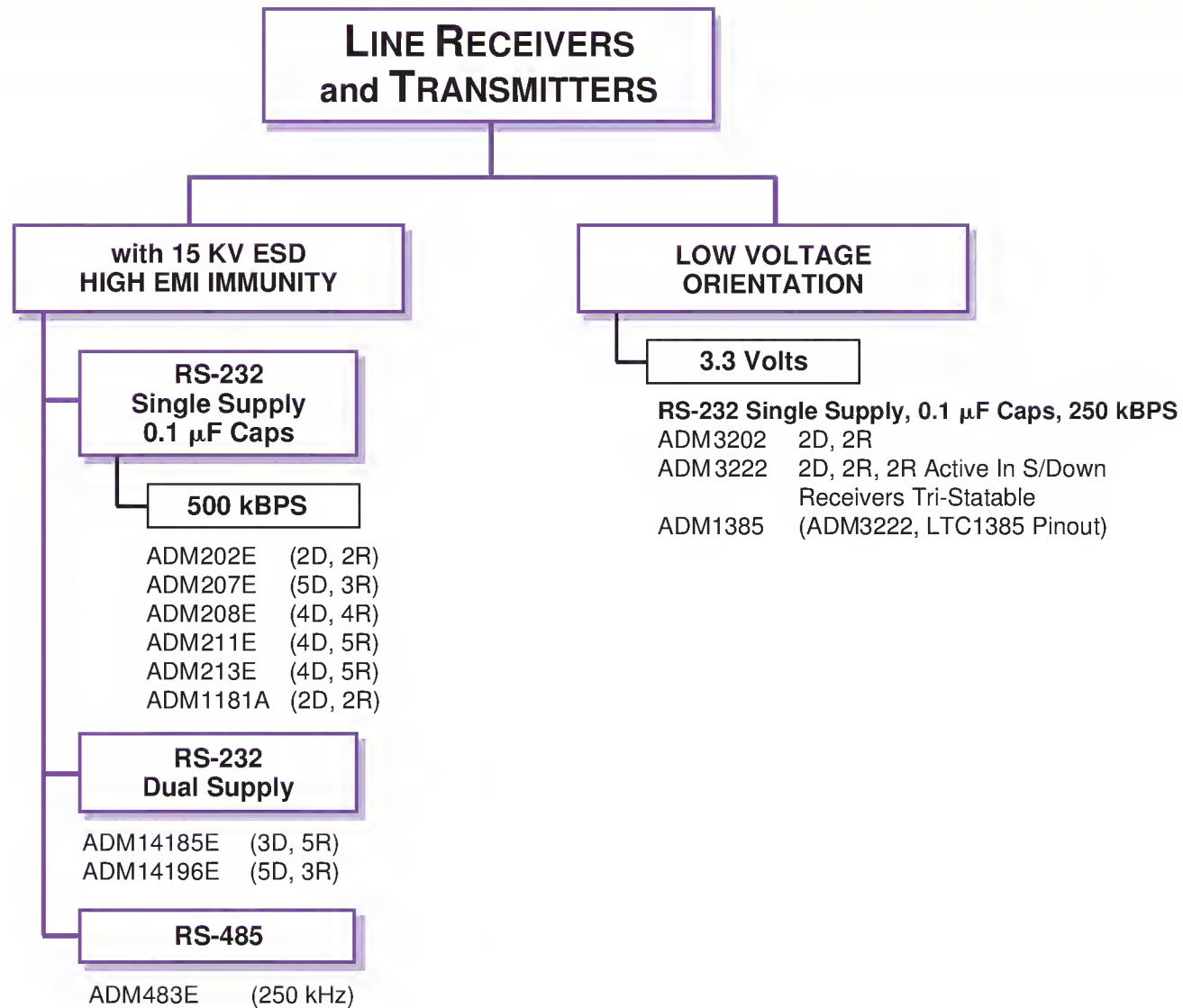
ADSSP-MOD 2000 (with AD1843)
ADSSP-MOD-2010

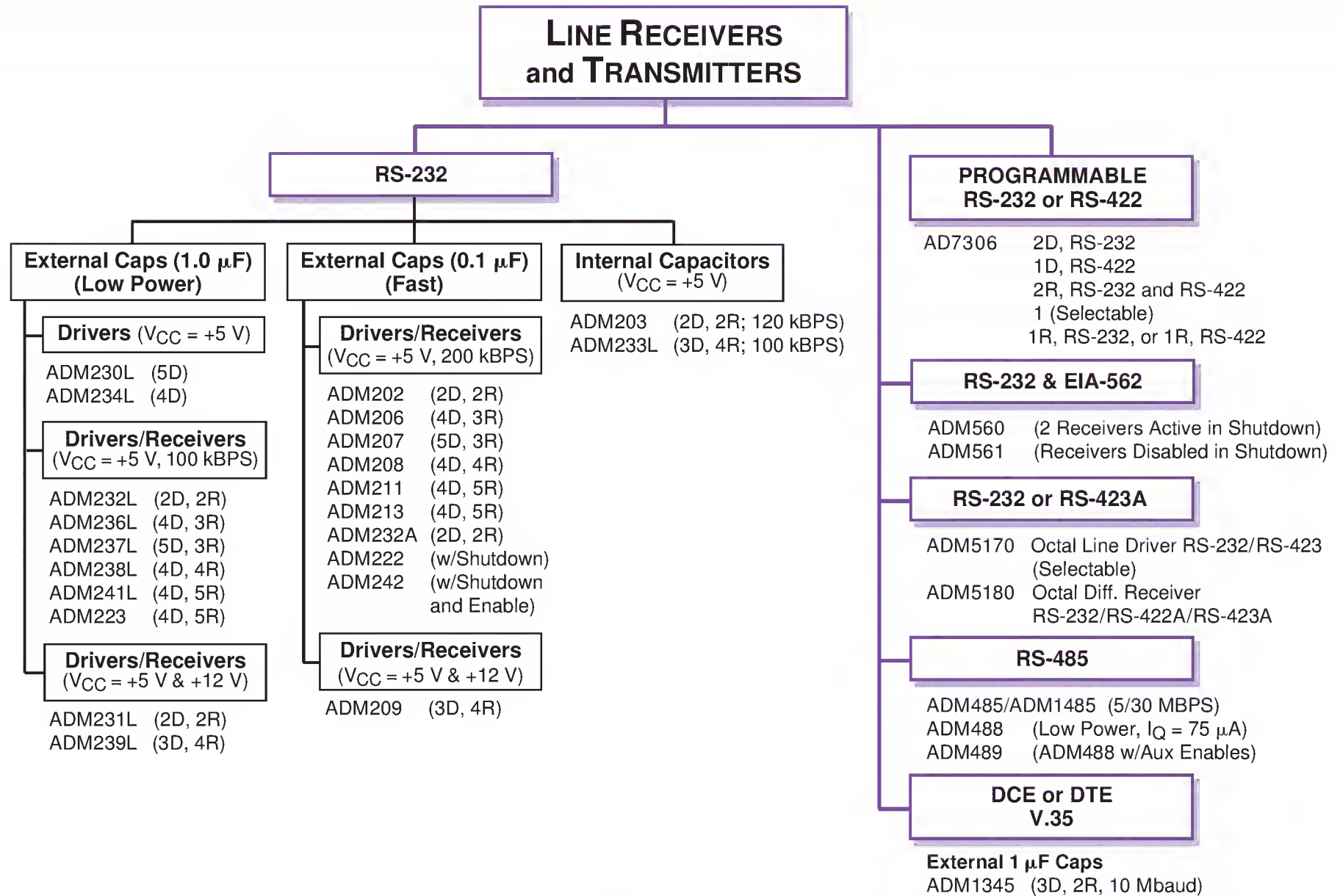
1 and 3 PHASE POWER METERS

ADSST-EM-1000 (Single Phase)
ADSST-EM-3000 (Three Phase)

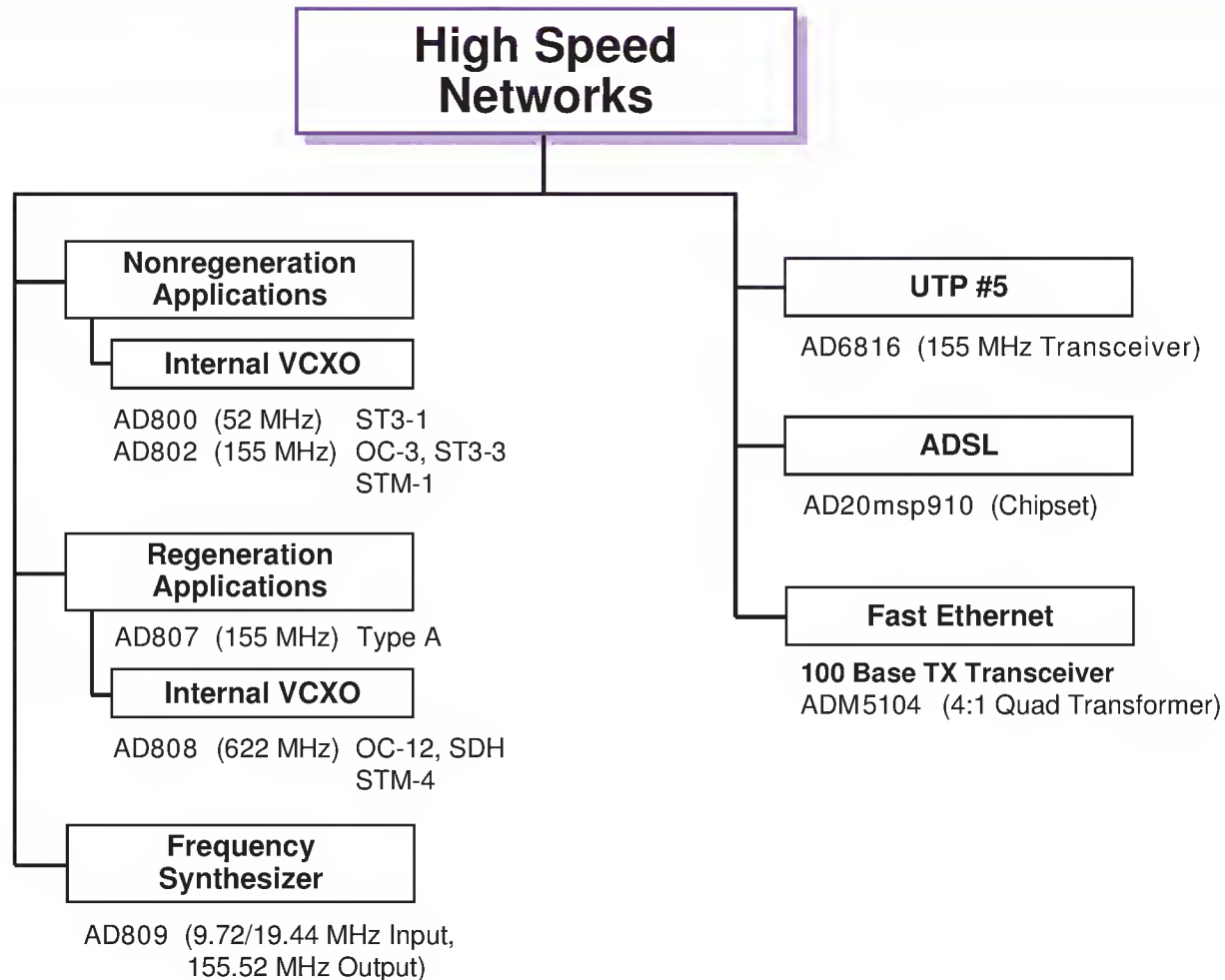
SOUND/COM CONTROLLER

AD1821EZ-KIT Audio and Host-Based
Modem (>>100 MHz)

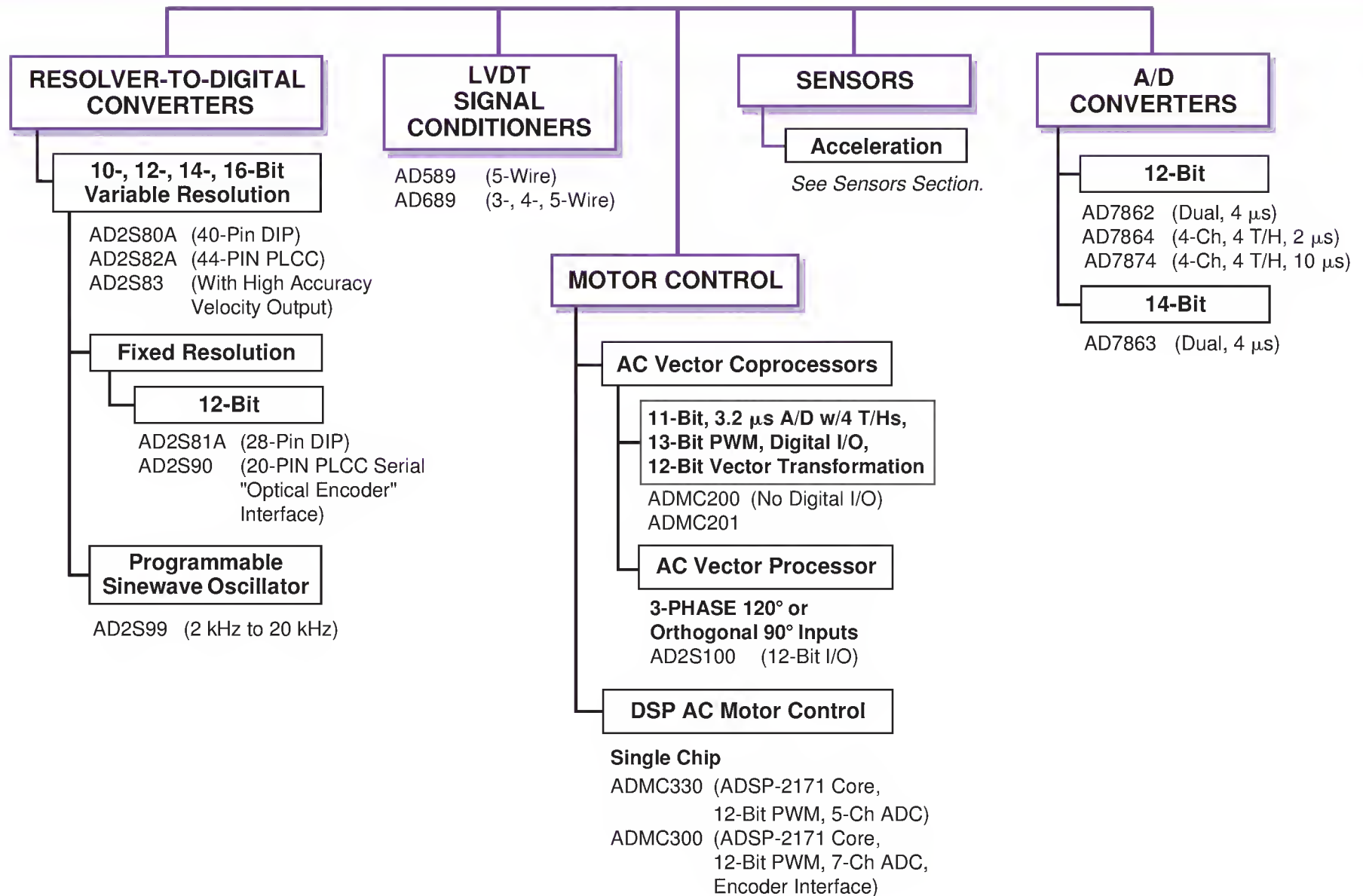




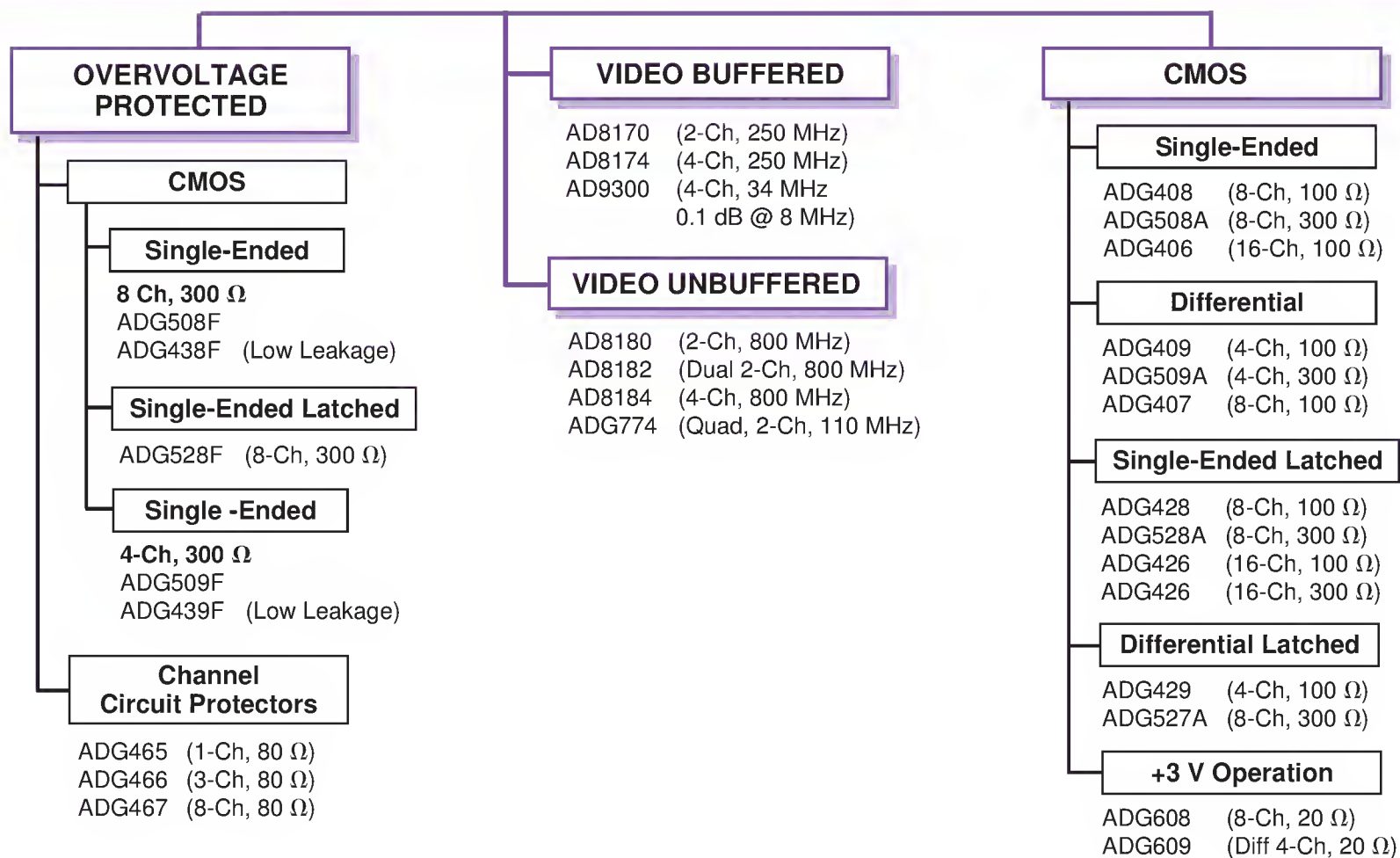
LANs: LOCAL/WIDE AREA NETWORKS



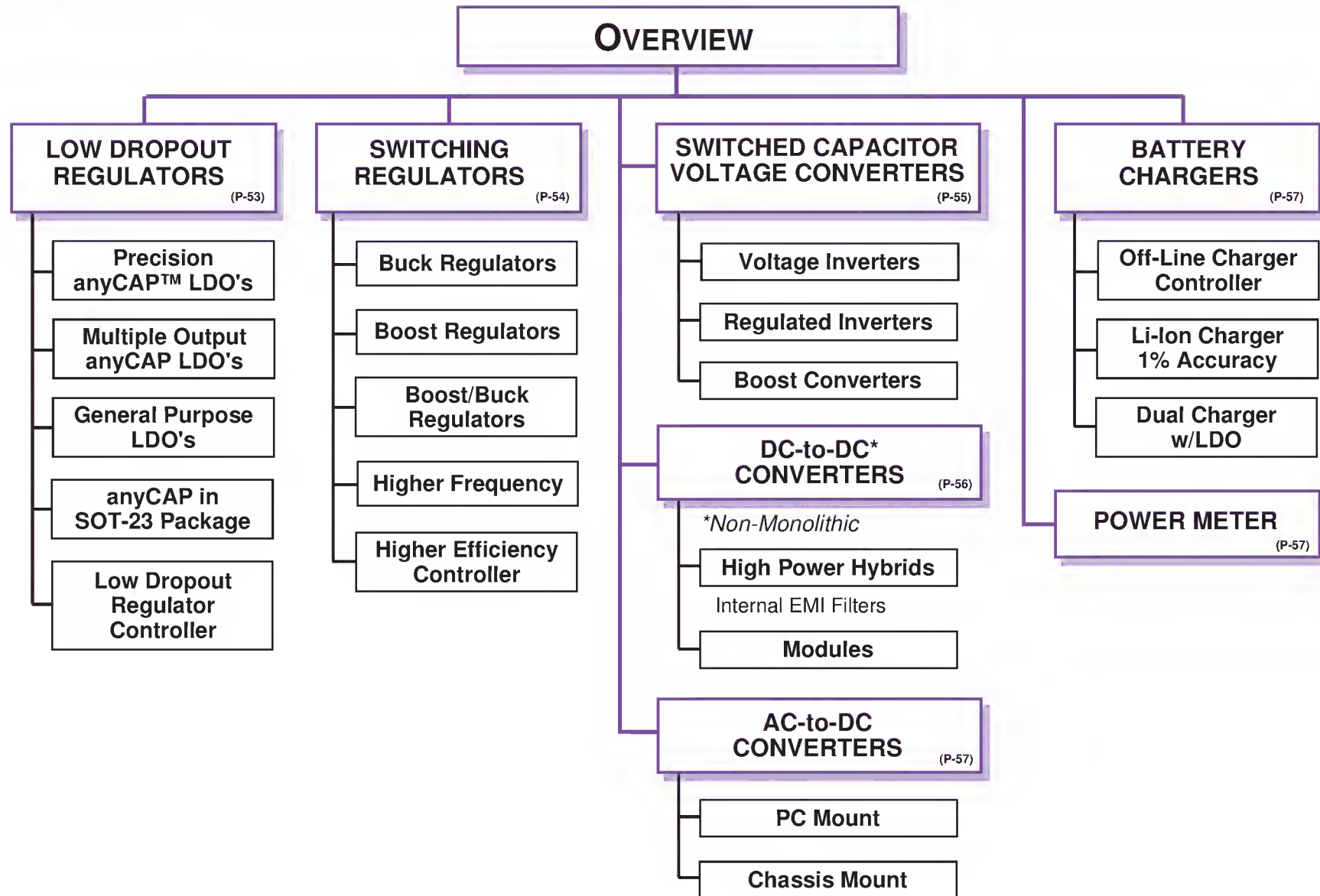
MOTION CONTROL INTERFACE



MULTIPLEXERS

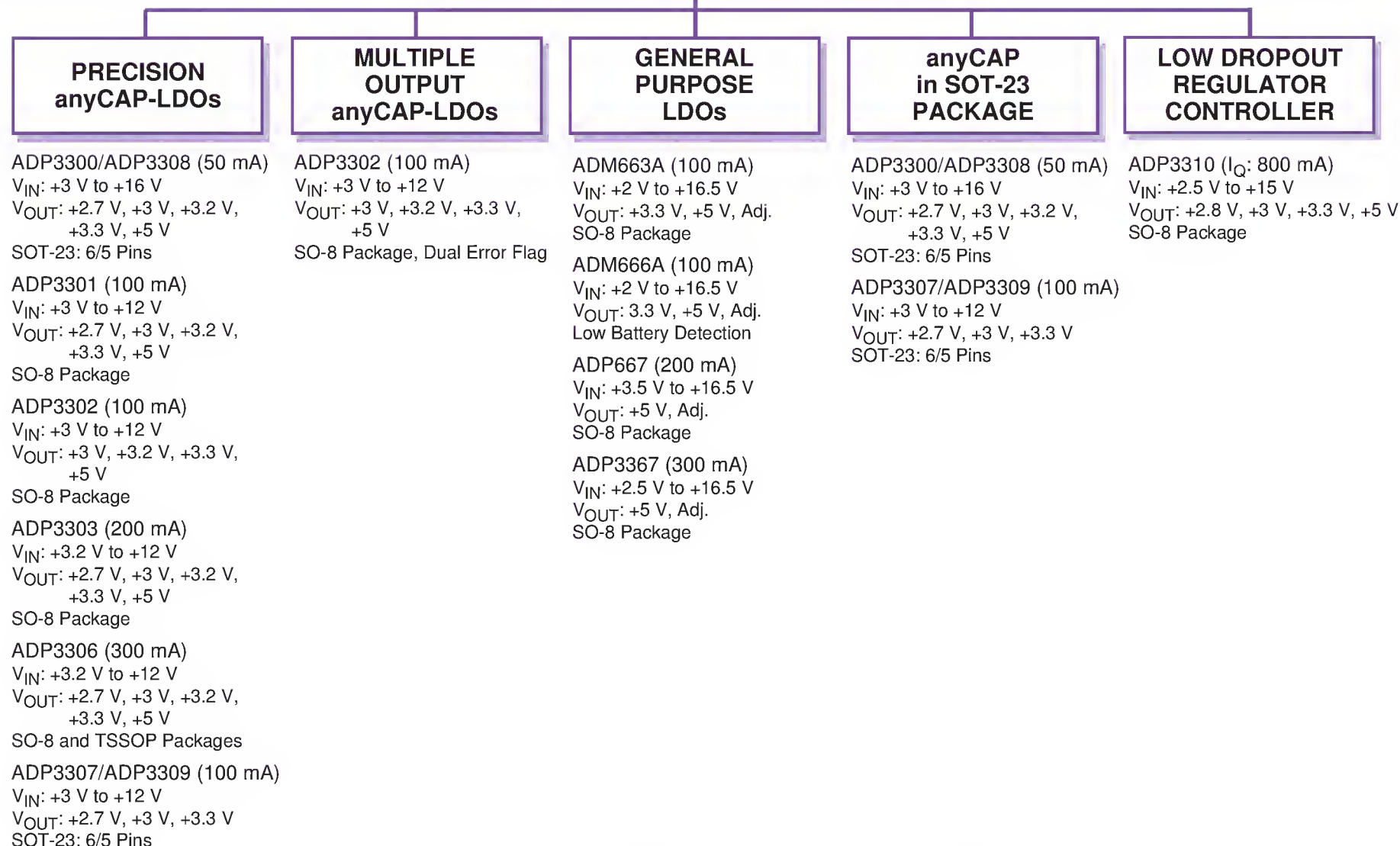


POWER MANAGEMENT

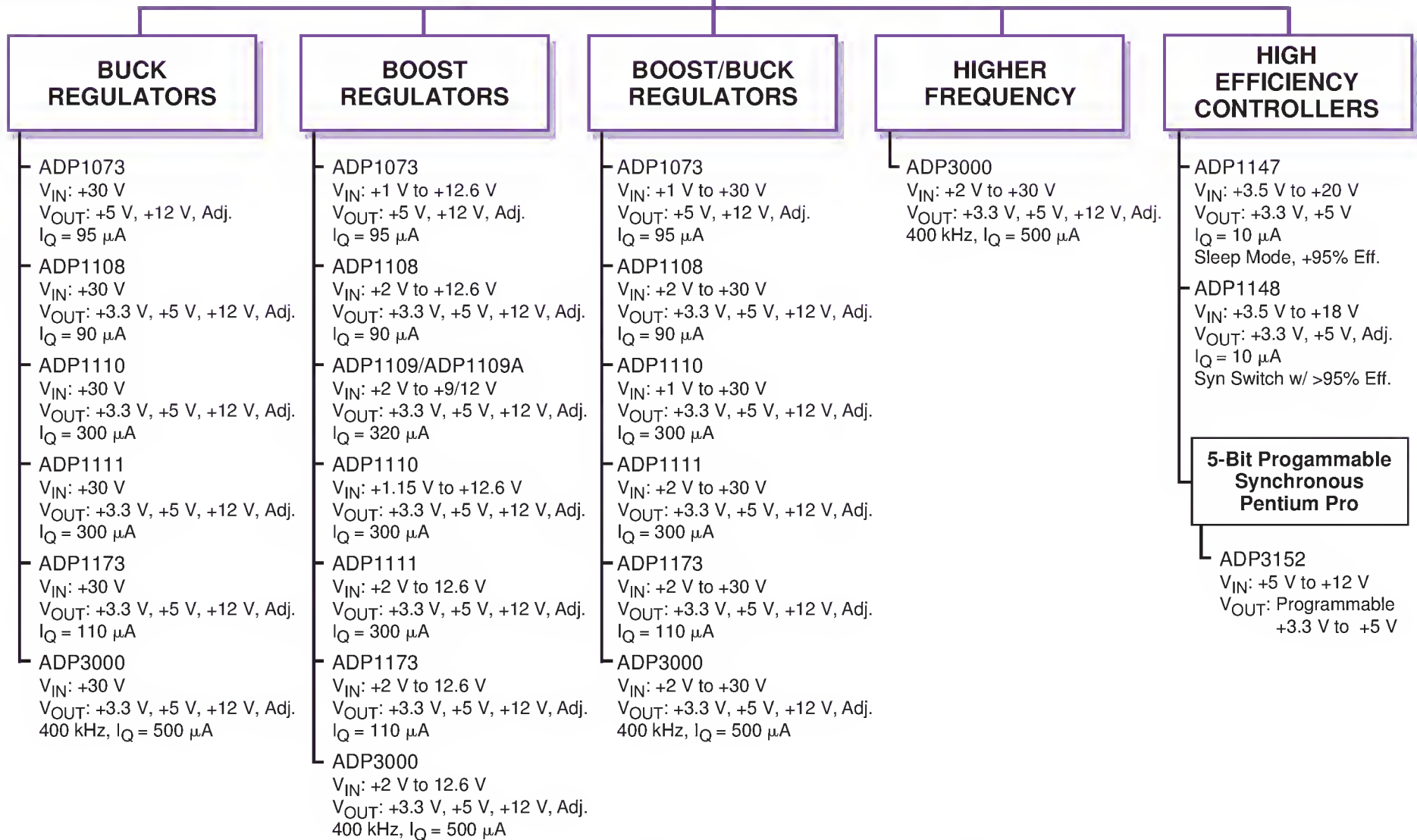


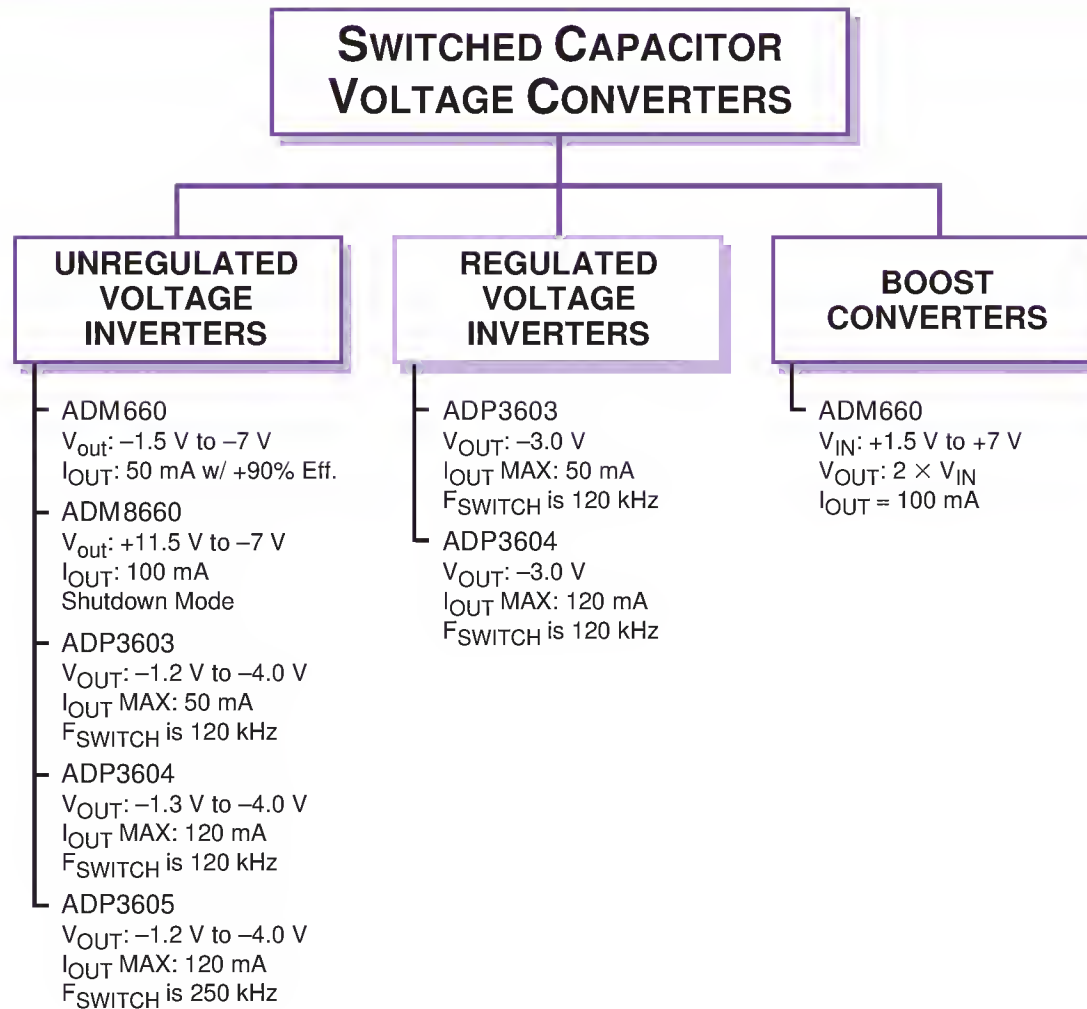
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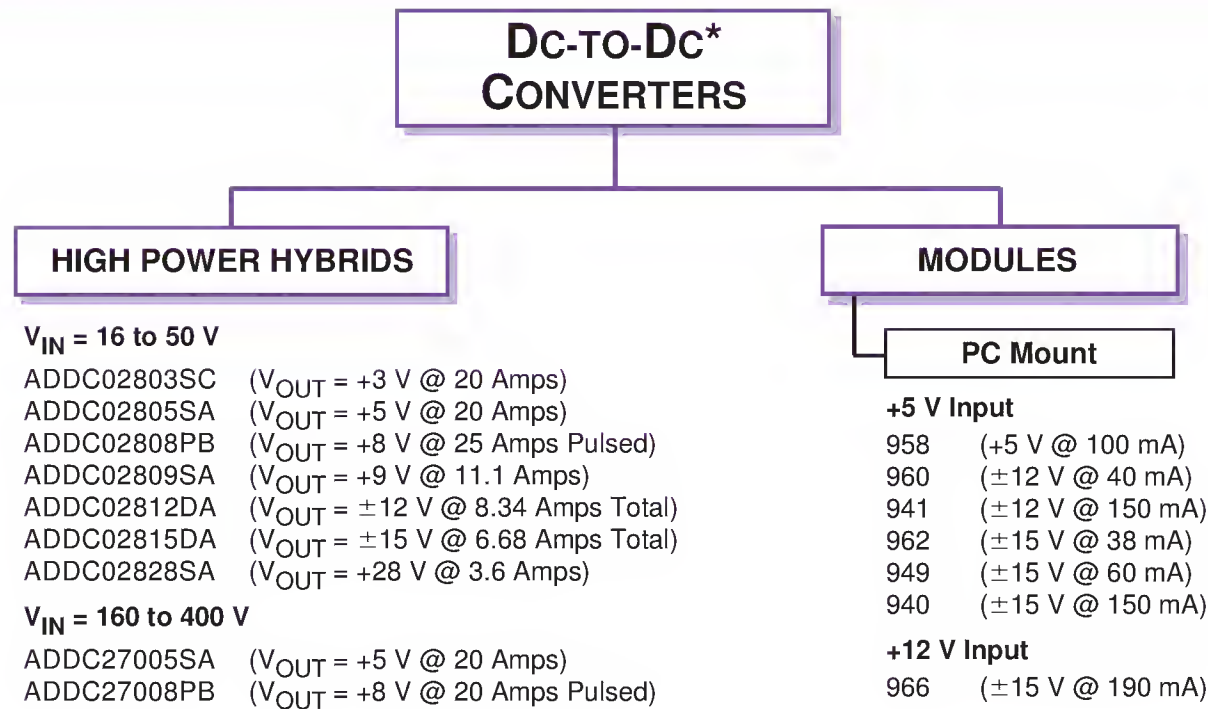
LOW DROPOUT REGULATORS



SWITCHING REGULATORS

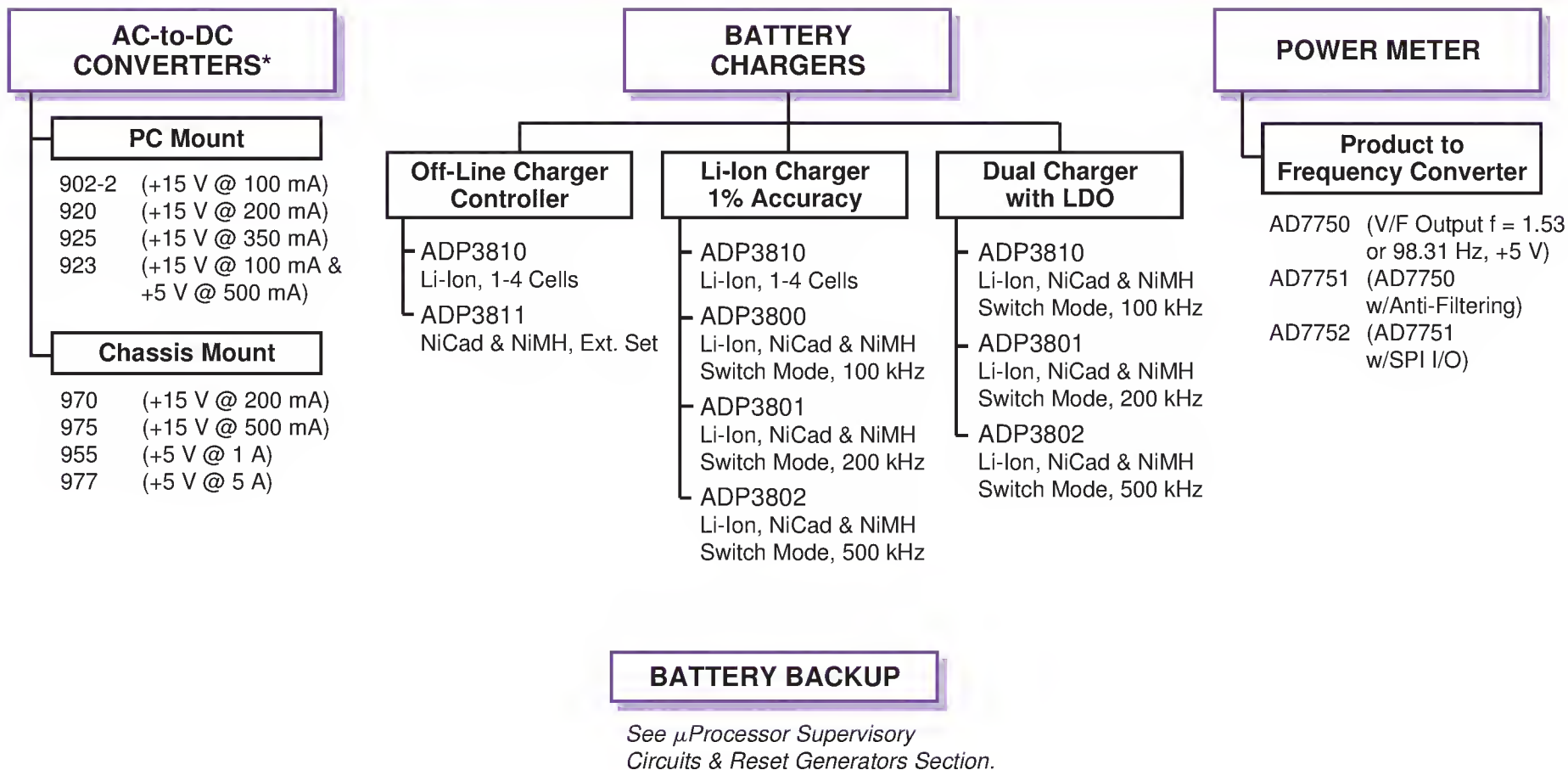




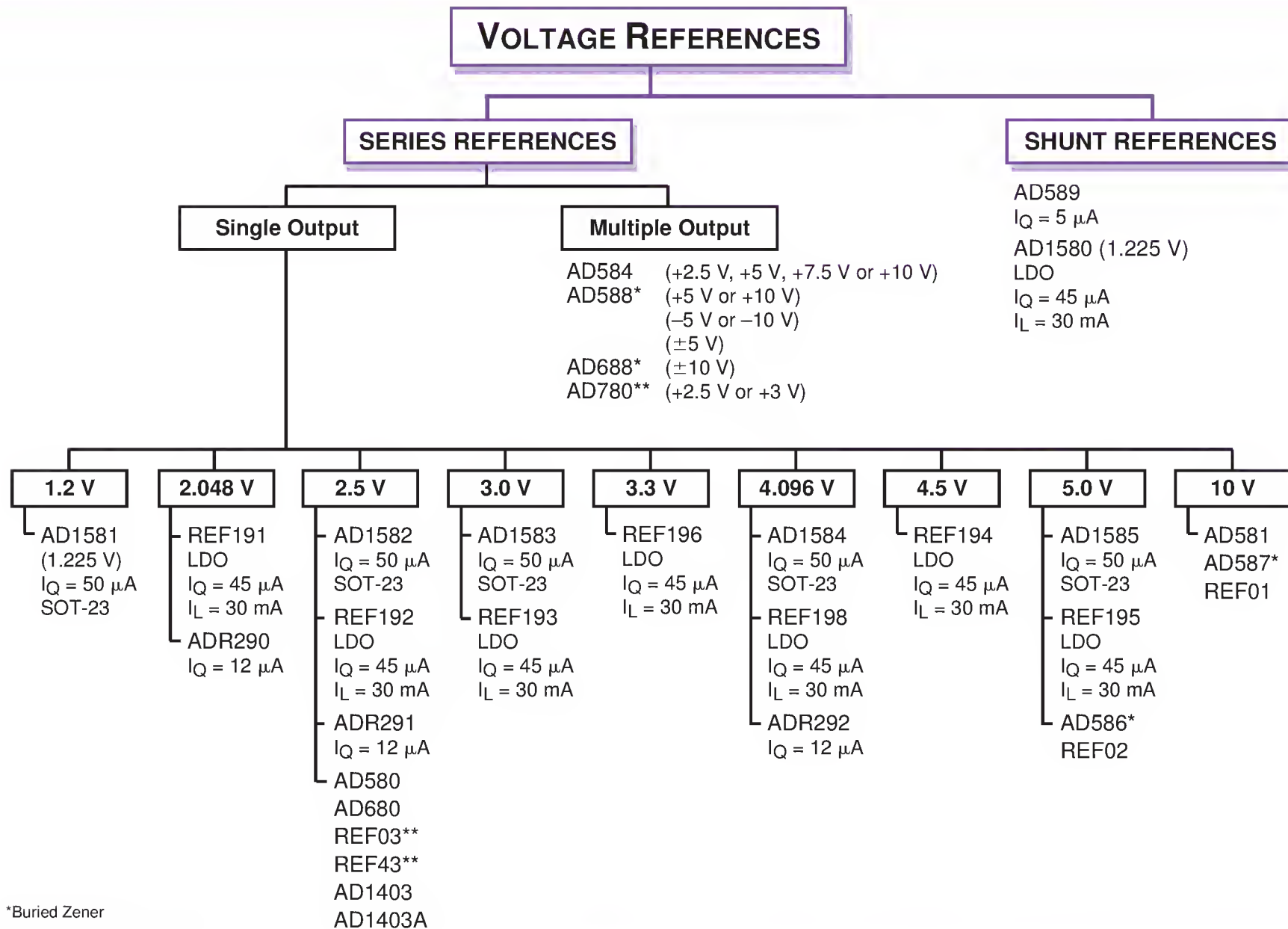


*Non-Monolithic Circuits

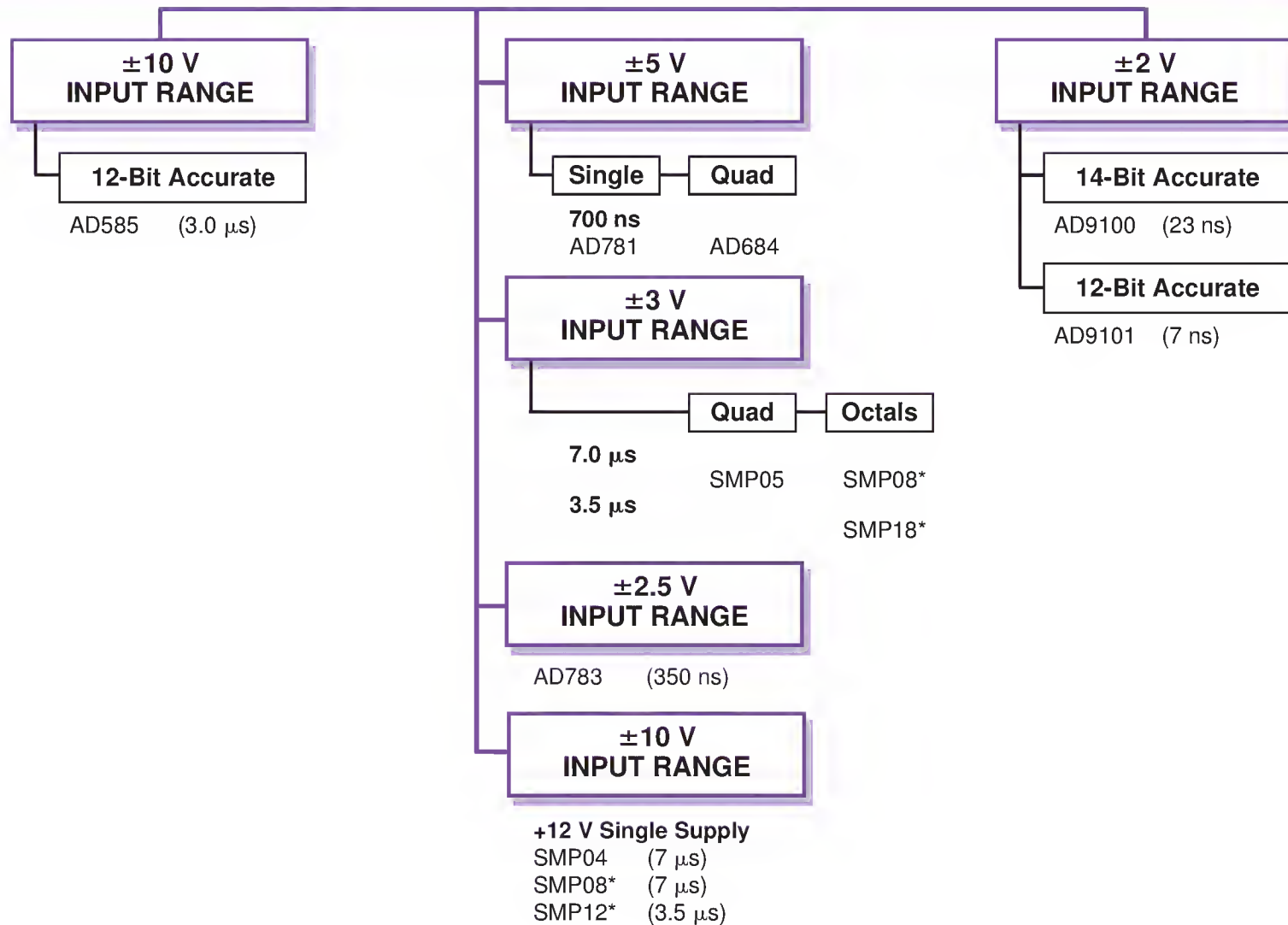
POWER MANAGEMENT



*Input Voltage 105 V AC to 125 V AC



SAMPLE/TRACK & HOLD AMPLIFIERS



*One Input, Eight Outputs

SENSORS & SIGNAL CONDITIONERS

SENSORS

ACCELERATION

Single Axis

ADXL05 ($\pm 5 g$, 5 mg Resolution)
 ADXL50 ($\pm 50 g$, 5 mg Resolution)
 ADXL181 ($\pm 5 g$, $\pm 50 g$,
 1 mg Resolution)

Dual Axis

ADXL250 ($\pm 5 g$ or $\pm 50 mg$,
 1 mg Resolution)

Evaluation Modules

See Selection Guides.

CURRENT

AD22001 (Light Bulb Monitor,
 5 Bulbs)

MAGNETIC

AD22151 (Prgm Gain 2.0 mV/G
 to 6 mV/G)

TEMPERATURE

Current Output

AD590 ($1 \mu A/^{\circ}K$ [TO-52])
 AD592 ($1 \mu A/^{\circ}K$ [TO-92])
 TMP17 ($1 \mu A/^{\circ}K$ [SOIC-8])
 ADT18 ($2 \mu A/^{\circ}K$ [SOT-23])
 ADT19 ($5 \mu A/^{\circ}K$ [SOT-23])

Ratiometric Output

AD22100 ($22.5 mV/^{\circ}C$, +5 V Supply)
 AD22103 ($28 mV/^{\circ}C$, +3 V Supply)

Voltage Output

AD22104 ($28 mV/^{\circ}C$, +3 V Supply)
 TMP3x Series (w Shutdown)
 TMP35 ($V_{OUT} = 250 mV, 10 mV/^{\circ}C$)
 TMP36 ($V_{OUT} = 750 mV, 10 mV/^{\circ}C$)
 TMP37 ($V_{OUT} = 500 mV, 20 mV/^{\circ}C$)
 ADT45 ($V_{OUT} = 250 mV, 10 mV/^{\circ}C$, SOT-23-3)
 ADT50 ($V_{OUT} = 750 mV, 10 mV/^{\circ}C$, SOT-23-3)

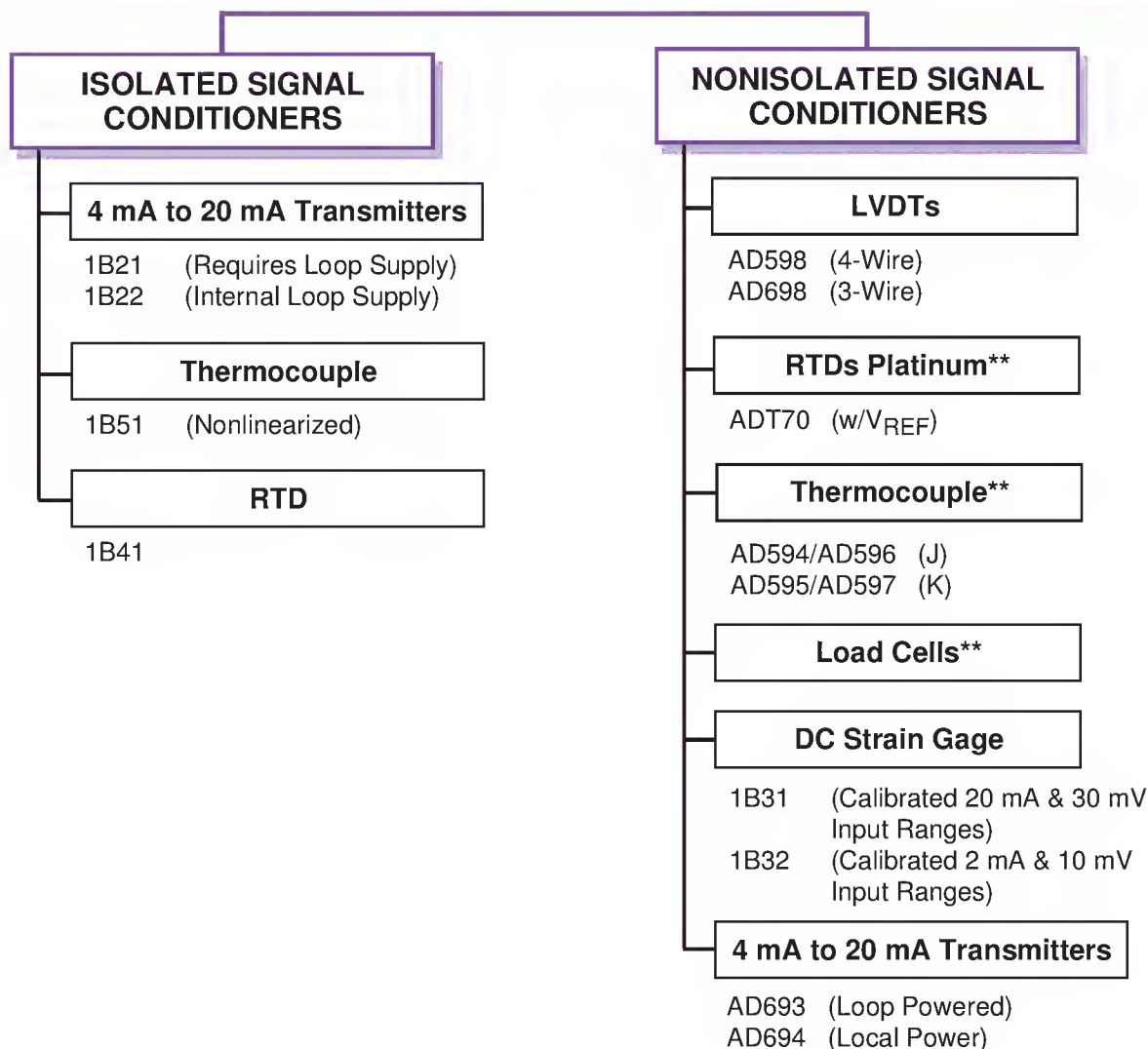
Digital Output

AD7416 (10-Bit A/D Output, I^2C I/O)
 AD7816 (10-Bit A/D Output, SPI I/O)
 TMP03 (PWM, Open Collector)
 TMP04 (PWM, TTL/CMOS)

TEMPERATURE CONTROLLERS

TMP01 ($^{\circ}F/^{\circ}C$ w/Window Comparator)
 TMP12 (w/Window Comparator,
 "Heater" Input)
 AD594/AD596 (J Thermocouple)
 AD595/AD597 (K Thermocouple)
 AD22105 (Programmable $-40^{\circ}C$ to $+125^{\circ}C$,
 1 Output)
 ADT05 (Programmable $-40^{\circ}C$ to $+125^{\circ}C$,
 1 Output, SOT-23-5)
 ADT10 (Programmable $-40^{\circ}C$ to $+125^{\circ}C$,
 1 Output, w/VPTAT Output)
 ADT14 (Programmable $-40^{\circ}C$ to $+125^{\circ}C$,
 4 Outputs, w/VPTAT Output)

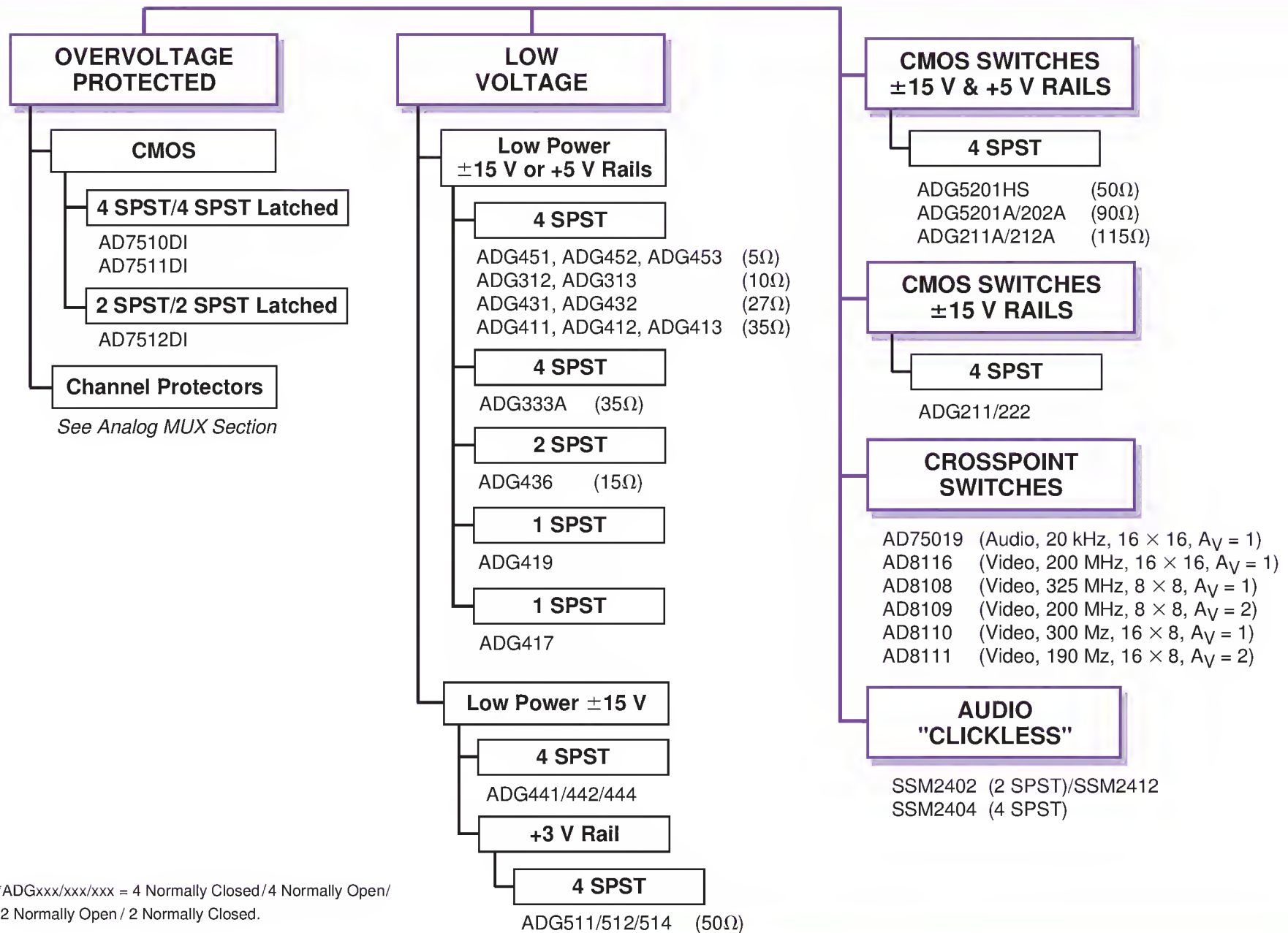
SENSORS & SIGNAL CONDITIONERS*



*See Industrial Catalog for 2B, 3B, 4B, 5B, 6B and 7B Series of Signal Conditioners

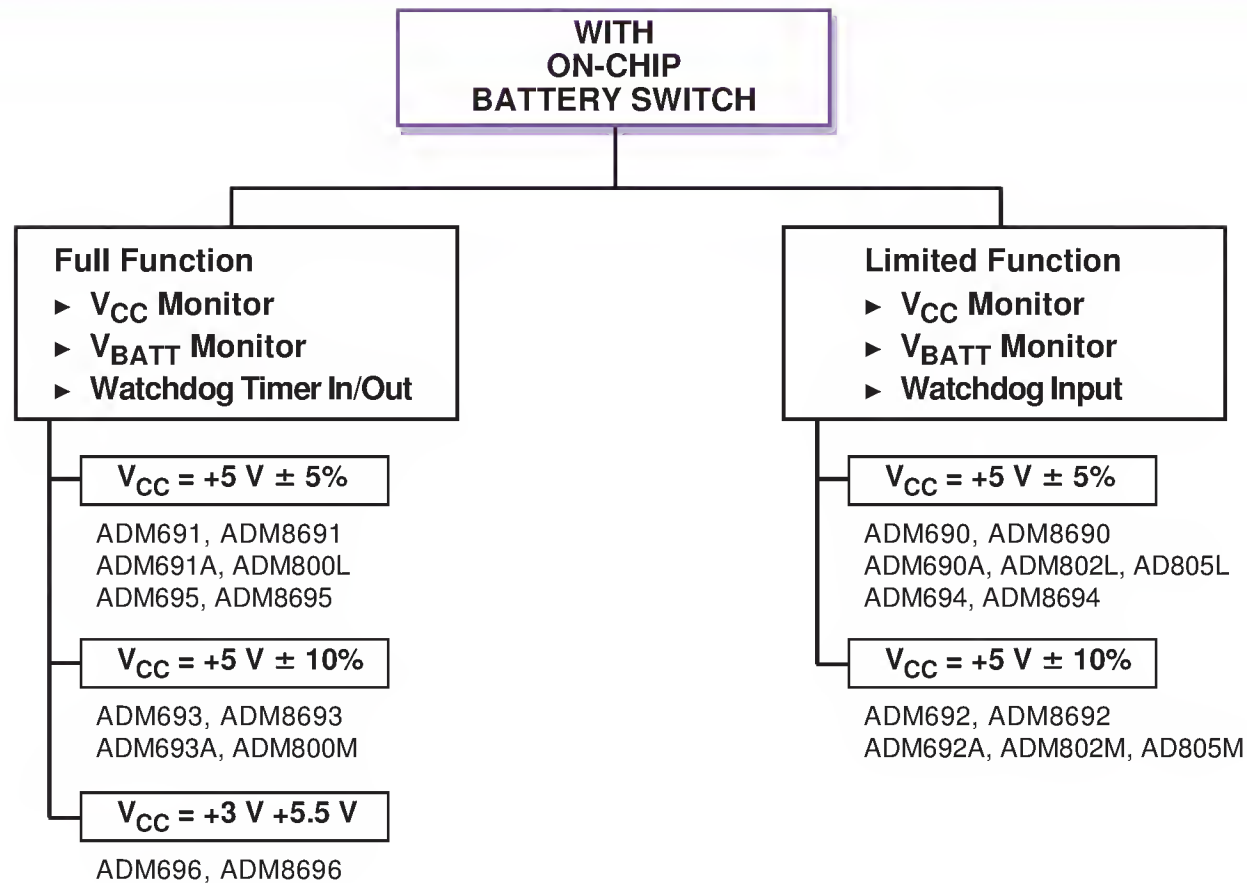
**See A/D Section, Sigma-Delta, for Signal Conditioning A/Ds.

SWITCHES

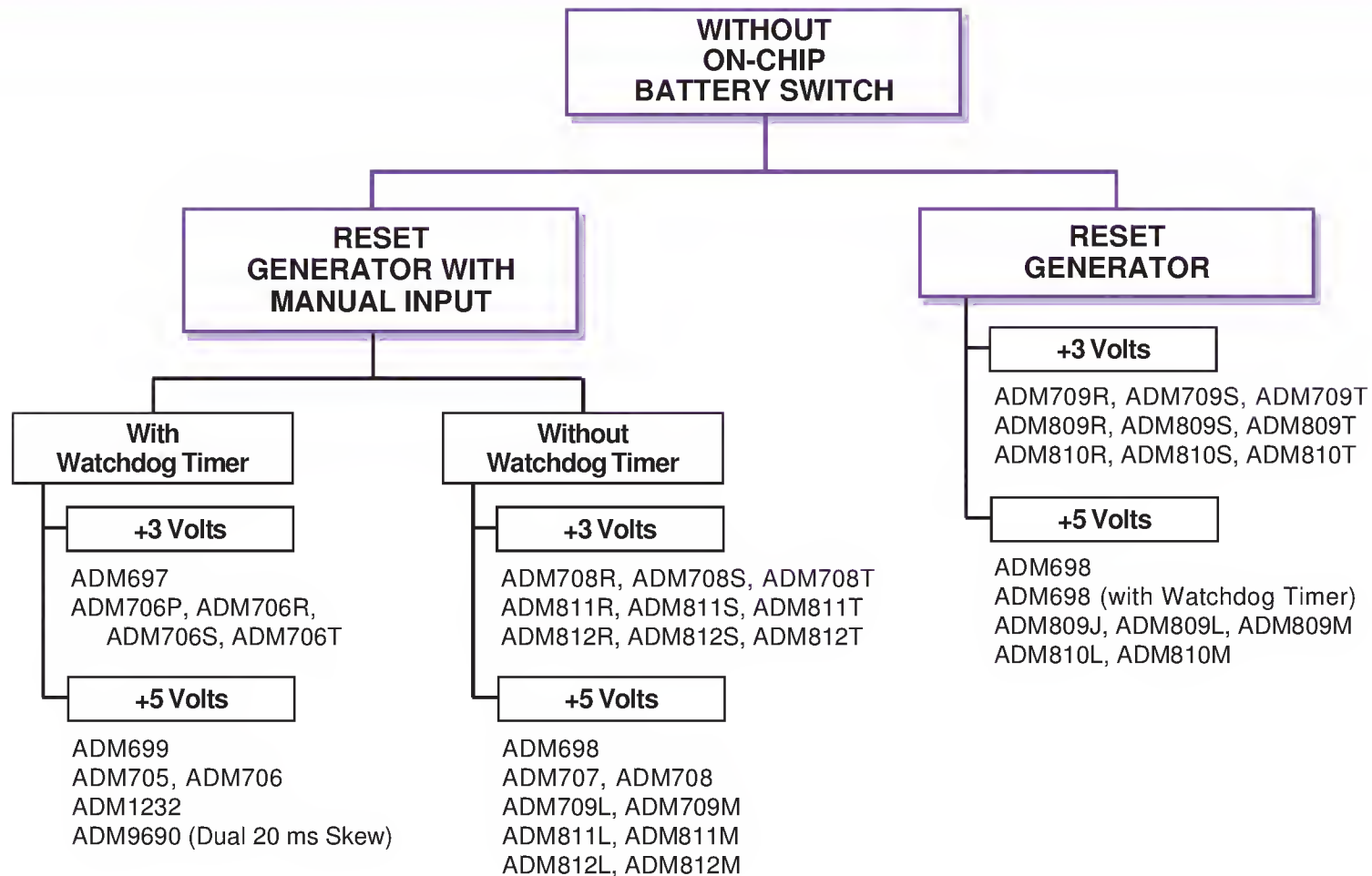


*ADGxxx/xxx/xxx = 4 Normally Closed/4 Normally Open/
2 Normally Open / 2 Normally Closed.

μPROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



μPROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



μPROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS

FUNCTION TABLE

With On-Chip V_{BATT} Switch

Circuit Functions	ADM8690 ADM690	ADM8691 ADM691	ADM8692 ADM692	ADM8693 ADM693	ADM8694 ADM694	ADM8995 ADM695	ADM8696 ADM696	ADM805L ADM802L ADM690A	ADM800L ADM691A	ADM805M ADM802M ADM692A	ADM800M ADM693A
Fixed Power Up/Down Reset	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Variable Power Up/Down Reset							✓				
Watchdog Timer Input	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Watchdog Timer Output		✓		✓		✓	✓		✓		✓
Power Failing Warning In/Out	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Reset & $\overline{\text{Reset}}$ Outputs		✓		✓		✓	✓		✓		✓
+3 V Systems											
$\overline{\text{CE}}$ In & $\overline{\text{CE}}$ Out		✓		✓	✓	✓			✓		✓
Reset & Watchdog Timebase		✓		✓		✓	✓		✓		✓
# of Pins	8	16	8	16	8	16	16	8	16	8	16
Low I _Q = 100 μA								✓	✓	✓	✓
Low Line Output		✓		✓		✓	✓		✓		✓

*SOT-23-3 Leads

μPROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS

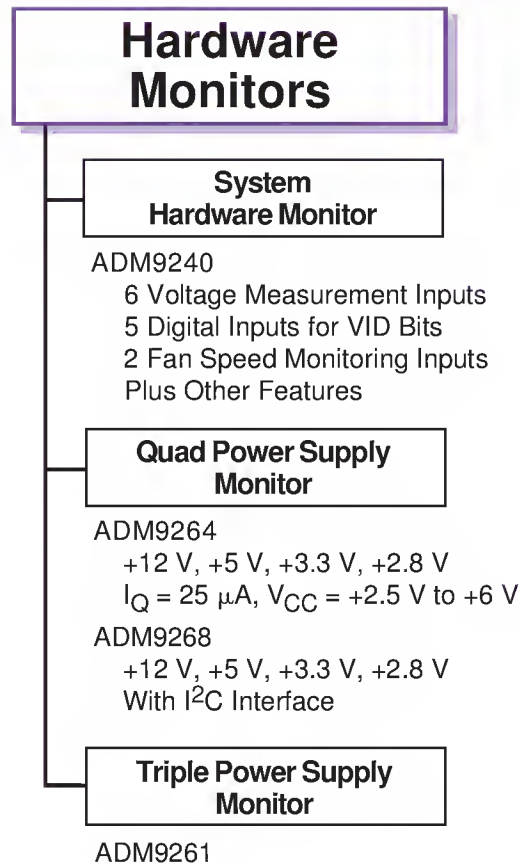
FUNCTION TABLE

Without On-Chip V_{BATT} Switch

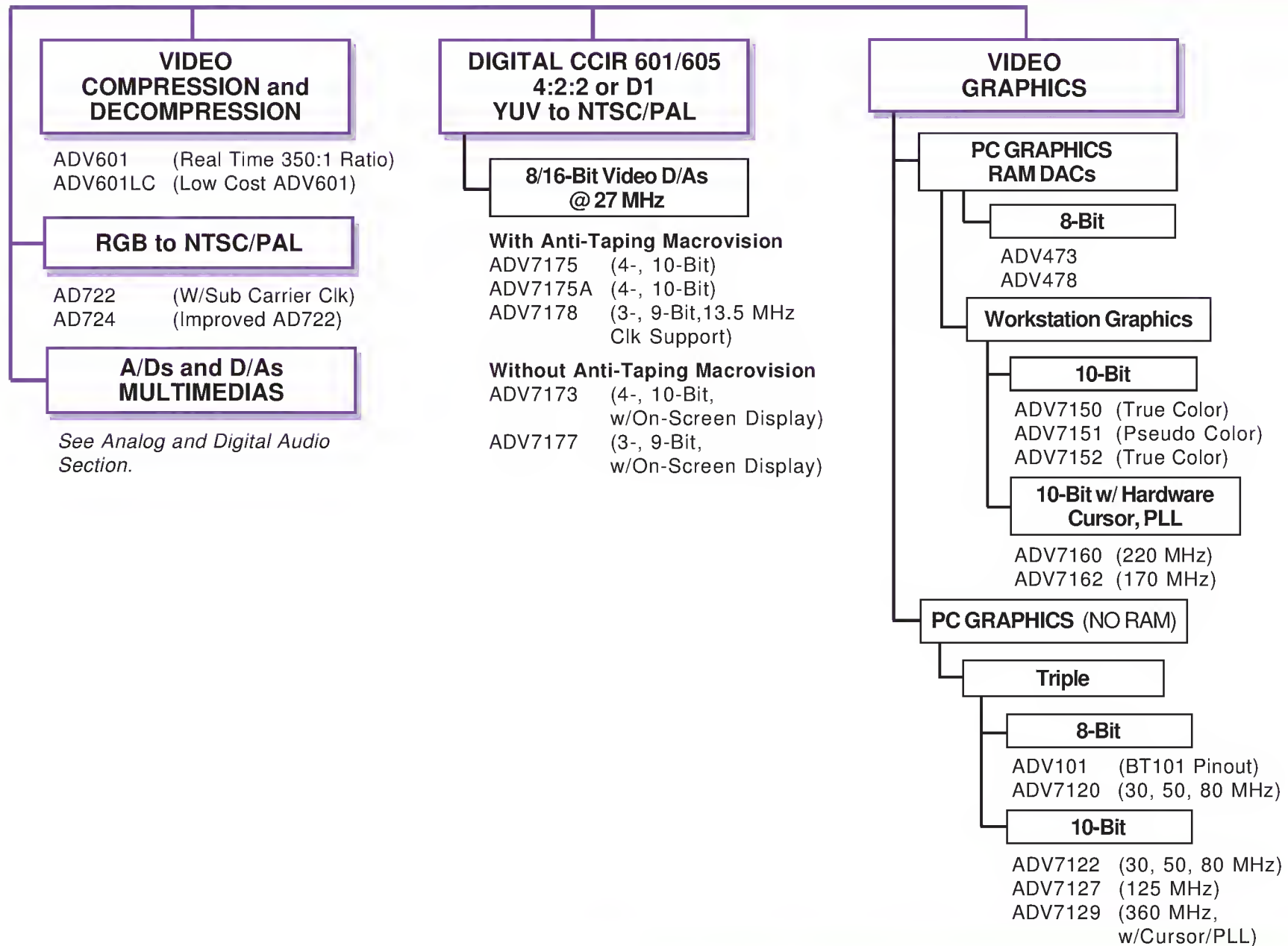
Circuit Functions	ADM8697 ADM697	ADM698	ADM699	ADM705	ADM706	ADM707	ADM708	ADM810* ADM809* ADM709	ADM9680	ADM1232 ADM1232LP	ADM811 ADM812
Fixed Power Up/Down Reset	✓	✓	✓	✓	✓	✓		✓	✓	✓	✓
Variable Power Up/Down Reset	✓										
Low Line Output	✓										
Watchdog Timer Input	✓		✓	✓	✓				✓	✓	✓
Watchdog Timer Output	✓		✓	✓	✓						
Power Failing Warning In/Out	✓			✓	✓	✓	✓				
Reset & Watchdog Timebase	✓			✓	✓				✓		
Manual Reset				✓	✓	✓	✓			✓	✓
+3 V Systems	✓				P, R, S, T		R, S, T	R, S, T			R, S, T
CE Out	✓										
# of Pins	16	8/16	16	8	8	8	8	3/8	8	8/16	4
Reset & Reset Outputs			✓			✓			✓		

*SOT-23-3 Leads

μ PROCESSOR SUPERVISORY CIRCUITS & RESET GENERATORS



VIDEO AND MULTIMEDIA



V/F & F/V CONVERTERS

V/F and F/V CONVERTERS

AD537 (150 kHz, w/Sq Wave Output)
AD654 (500 kHz, w/Sq Wave Output)
ADVFC32 (500 kHz)
AD650 (1 MHz)

Synchronous

AD652 (2 MHz Clock)
AD7741 (5 MHz Clock)
AD7742 (5 MHz Clock, Bipolar Input)

Power Meter

See Power Management Section.